

Service Manual

**(9 Inch) LCD Color Monitor &
DVD Player & DVB-T&ATV
mp-man PDV-TY995**



Specification..... 2

Block Diagram..... 4

General Alignment Instruction..... 5

Troubleshooting..... 8

Printed Circuit Board..... 12

Explode View..... 13

IC Block Diagram and Lead Identification..... 26

Schematic Diagram..... 37

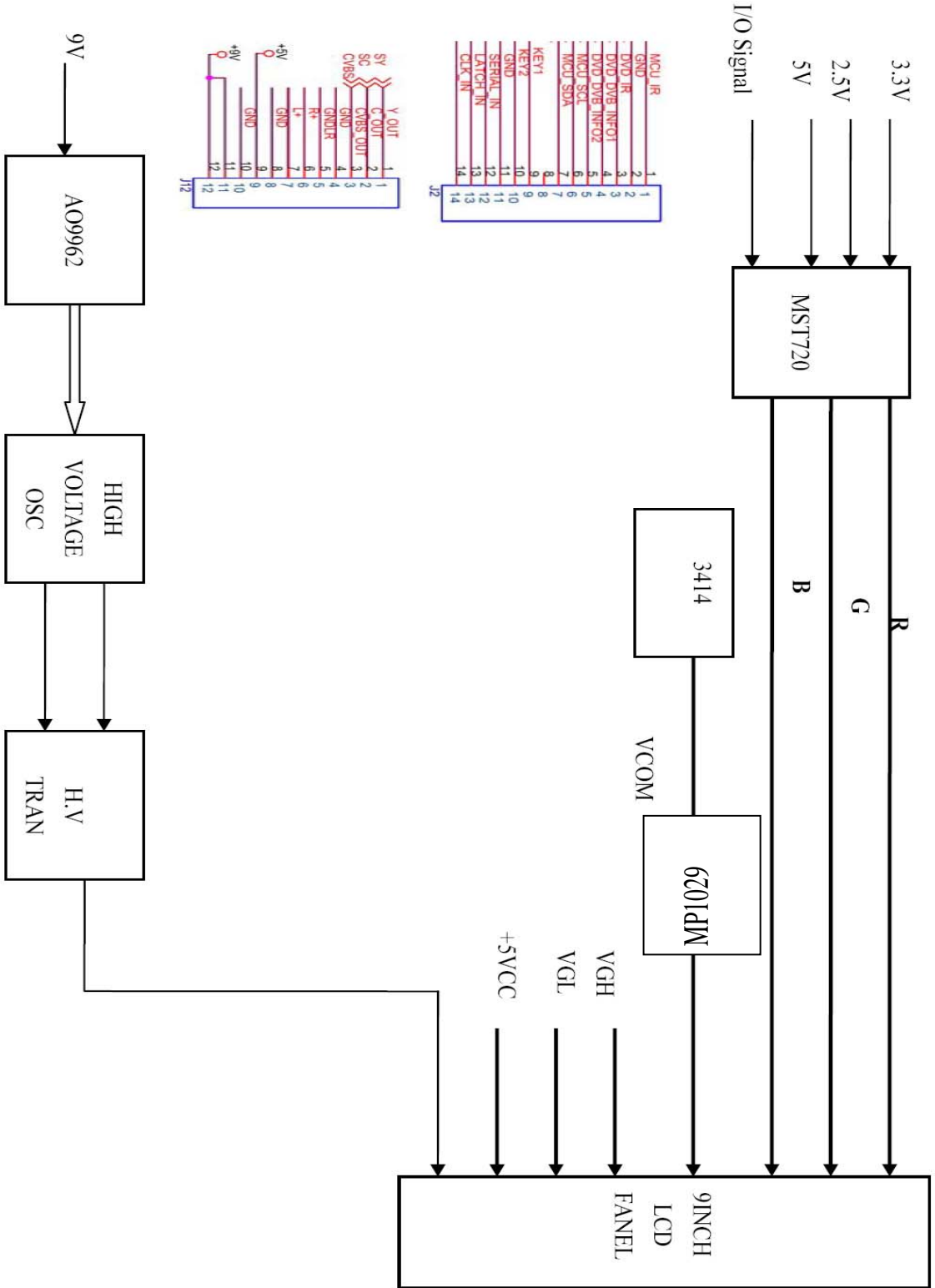
SPECIFICATION

Description		Condition	Unit	Limit	Nominal	Result
Video Signal Level		75% color bar, 75 Ohm	V _{p-p}	0.7 ± 0.1	0.7	
SYNC Lever			V _{p-p}	0.3 ± 0.1	0.3	
Video out level			V _{p-p}	1.0 ± 0.2	1.0	
Video out level (No Loader)			V _{p-p}	2.0 ± 0.2	2.0	
S-VIDEO out level		Y (100% color bar)	mV	700 ± 140	700	
		C (100% color bar)	mV	880 ± 176	880	
Audio out level	CD	1KHz/0dB	V	0.6 ± 0.2	0.7	
	DVD	1KHz/0dB	V	1.4 ± 0.2	1.25-1.4	
Frequency Response (20 Hz - 20 kHz)		20Hz	dB	$\leq \pm 3$	0	
		125Hz	dB	$\leq \pm 3$	0	
		10KHz	dB	$\leq \pm 3$	0	
		20KHz	dB	$\leq \pm 3$	0	
Signal-to-noise ratio (add A)		Infinity zero/-&dB/L&R	dB	≥ 75	≥ 75	
Audio distortion & noise		1KHz/0dB/L&R	dB	≤ -65	-65	
Dynamic range		1KHz/-60dB	dB	≥ 85	≥ 85	
R/L Cross sound		1KHz/0dB/L&R	dB	≥ 45	≥ 45	
1 kHz Channel Unbalance		1KHz/0dB/L&R	dB	≤ 3	0	
Power Consumption DC 9V		Standby	W	0	0	
		Rating (DVD+Monitor)	W	≤ 12	≤ 12	
Eccentricity		A-BEX TDV-552	um	≥ 100	100	
Scratch		A-BEX TDV-541	mm	≥ 1.6	1.6	
Black dot		A-BEX TDV-PW525CW	mm	$\geq \varnothing 0.8$	0.8	
Finger Print		A-BEX TDV-PW525CW	um	$\geq \varnothing 65$	65	
Vertical Deviation		A-BEX TDV-MW533CW	mm	≥ 0.6	0.6	
HEADPHONE OUTPUT		1KHz,400mV	mW	3 ± 2	3	

HEADPHONE Noise		No Signal	mV	< 1mV	< 1mV	
SPK MAX Output (DVD)	L	1KHz/0dB	mW	500±100	500	
	R	1KHz/0dB	mW	500±100	500	
SPK MAX Output (AV IN)	L	1KHz/0dB	mW	500±100	500	
	R	1KHz/0dB	mW	500±100	500	
HEADPHONE S/N (add A)		Infinity zero/-&dB/L&R	dB	≥75	75	
SPK S/N (add A)		Infinity zero/-&dB/L&R	dB	≥50	50	
HEADPHONE RESP (20Hz~20KHz)		20Hz	dB	≤±3	0	
		125Hz	dB	≤±3	0	
		10KHz	dB	≤±3	0	
		20KHz	dB	≤±3	0	
Amplifier Freq Resp (20Hz~20KHz)		20Hz	dB	≤±3	0	
		125Hz	dB	≤±3	0	
		10KHz	dB	≤±3	0	
		20KHz	dB	≤±3	0	

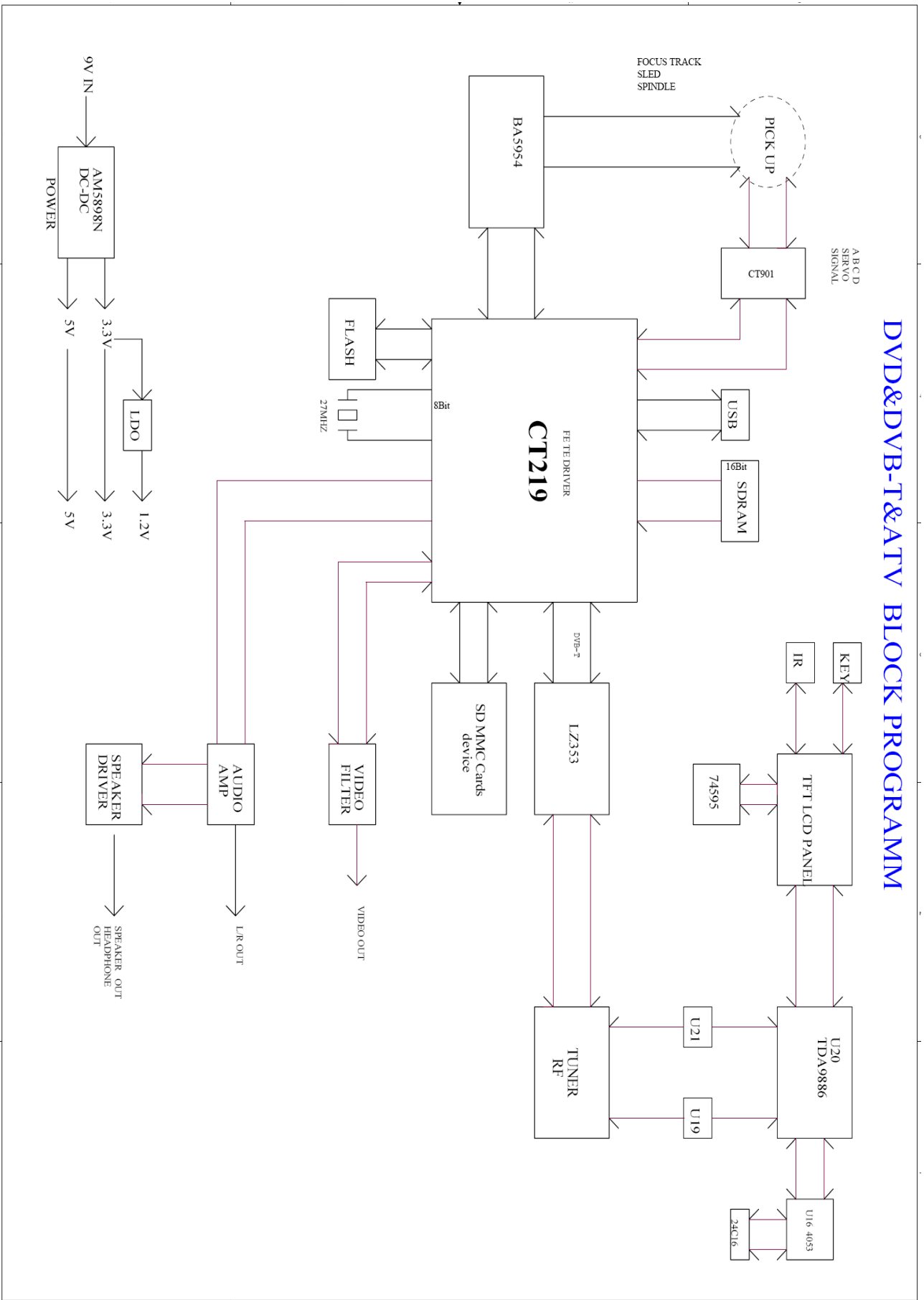
BLOCK DIAGRAM

MONITOR



DVD&DVB-T&ATV PART

DVD&DVB-T&ATV BLOCK PROGRAMM



GENERAL ALIGNMENT INSTRUCTION

The Main PCB of Monitor Modification:

1. Input voltage is 6.8V-16V, input signal is test circuit BY 5418.
2. Adjust VR2 to get good performance.

TROUBLESHOOTING

LCD

SYMPTOM	CAUSE	REMEDY
LCD MONITOR PART		
1) Picture distortion	✂ Defective capacitor (U10)	✂ Replace capacitor MST720A.
2) No Picture	✂ Defective IC(U1)	✂ Replace IC AO9926
3) No Picture, Picture no Good	✂ Defective IC (U12, 3414).	✂ Replace IC 3414.
4) Luminesce no good	✂ Defective U4	✂ Replace IC ASM1117
5) Color is no good	✂ Defective Y2.	✂ Replace Y2.
6) SYNC no good	✂ Defective U10	✂ Replace IC MST720A
7) TINT no good	✂ Defective U10	✂ Replace IC MST720A
8) Picture no good	✂ Defective U10	✂ Replace IC MST720A
9) Brightness no good	✂ Defective U10	✂ Replace IC MST720A
10) Key no action	✂ Defective U11	✂ Replace IC 25LV512

TROUBLESHOOTING

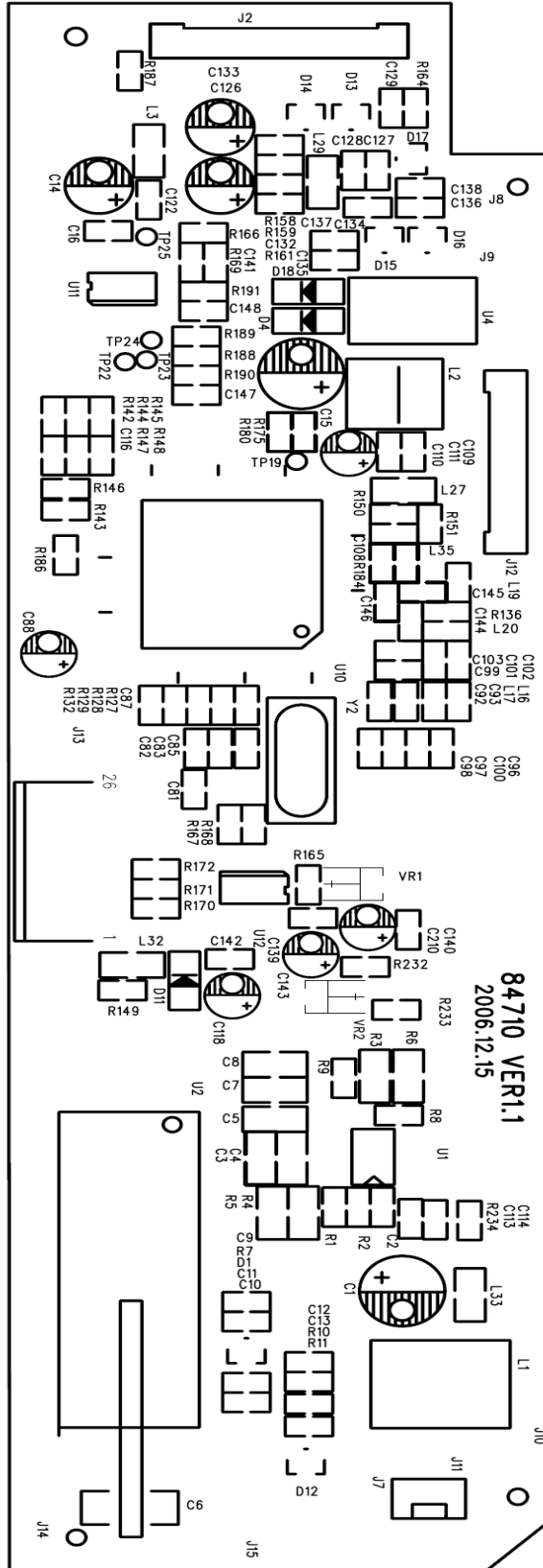
SYMPTOM	CAUSE	REMEDY
DVD PART		
1) No Power	<ul style="list-style-type: none"> ✘ Power source is not correct. The positive and negative does not match the unit. ✘ Power button defective. ✘ Defective triode U31,U32 ✘ Defective U29 	<ul style="list-style-type: none"> ✘ Replace power source. ✘ Charge the position of positive and negative. ✘ Replace power button. ✘ Replace U31,U32(cd4060) ✘ Replace IC 74595
2) No Picture	<ul style="list-style-type: none"> ✘ Defective U30 (24C16N). ✘ Defective U35. ✘ Defective IC (U34,CT219). 	<ul style="list-style-type: none"> ✘ Replace U30 (24C16N). ✘ Replace U35(SDRAM). ✘ Replace IC CT219.
3) No Sound	<ul style="list-style-type: none"> ✘ Defective U23 ✘ Defective U28 ✘ Defective U29 	<ul style="list-style-type: none"> ✘ Replace IC (C4558). ✘ Replace IC (TDA2822) ✘ Replace IC 74595
4) Sound no good	<ul style="list-style-type: none"> ✘ Defective U23 (C4558). ✘ Defective U28 (TDA2822) 	<ul style="list-style-type: none"> ✘ Replace U23 (C4558). ✘ Replace U28 (TDA2822)
5) Can not read DISC	<ul style="list-style-type: none"> ✘ Defective U37(CD5954). ✘ PICK-up Laser is defective. ✘ Defective U34 (CT219). 	<ul style="list-style-type: none"> ✘ Replace U37 (CD5954) ✘ Replace pick up. ✘ Replace U34 (CT219)
6) No remote control function	<ul style="list-style-type: none"> ✘ Defective IR1. ✘ Key is Defective. ✘ Defective MONITOR U11 	<ul style="list-style-type: none"> ✘ Replace IR1. ✘ Replace keys. ✘ Replace IC 25LV512
7) Can not read CARD	<ul style="list-style-type: none"> ✘ Defective J19 ✘ Defective U43 ✘ Defective U34 (CT219). 	<ul style="list-style-type: none"> ✘ Replace J19 ✘ Replace IC AU6332 ✘ Replace U34 (CT219)
8) Can not read USB1	<ul style="list-style-type: none"> ✘ Defective USB1 ✘ Defective U43 ✘ Defective U34 (CT219). 	<ul style="list-style-type: none"> ✘ Replace USB1 ✘ Replace IC AU6332 ✘ Replace U34 (CT219)

TROUBLESHOOTING

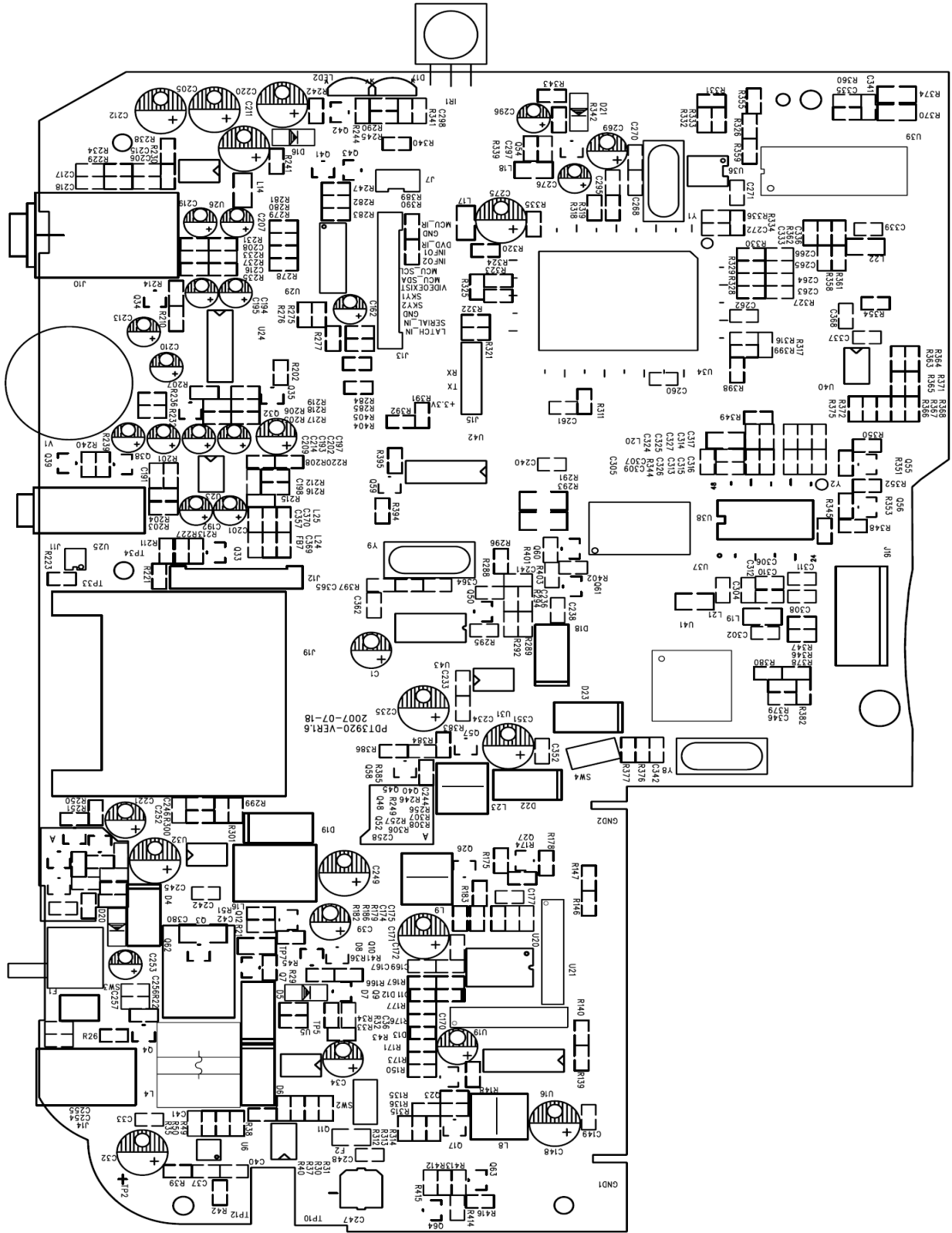
SYMPTOM	CAUSE	REMEDY
DVD PART		
9)Can not AV IN	<ul style="list-style-type: none"> ✕ Defective U25 ✕ Defective MONITOR U11 ✕ Defective U29 	<ul style="list-style-type: none"> ✕ Replace IC TSSA23157 ✕ Replace IC 25LV512 ✕ Replace IC 74595
10)Can not receive ATV signal	<ul style="list-style-type: none"> ✕ Defective TUNER1. ✕ Defective U16 ✕ Defective U29 ✕ Defective U20 ✕ Defective MONITOR U11 	<ul style="list-style-type: none"> ✕ Replace TUNER1. ✕ Replace IC 24C16 ✕ Replace IC74595 ✕ Replace IC TDA9886 ✕ Replace IC 25LV512
11) ATV signal no good & ATV Picture no good	<ul style="list-style-type: none"> ✕ Defective TUNER1. ✕ Defective U21 ✕ Defective U20 	<ul style="list-style-type: none"> ✕ Replace TUNER1. ✕ Replace IC K3953 ✕ Replace IC TDA9886
12) ATV Sound no good	<ul style="list-style-type: none"> ✕ Defective TUNER1. ✕ Defective U19 ✕ Defective U20 	<ul style="list-style-type: none"> ✕ Replace TUNER1. ✕ Replace IC K9653 ✕ Replace IC TDA9886
13)Can not receive DVB-T signal	<ul style="list-style-type: none"> ✕ Defective TUNER1. ✕ DefectiveU41 ✕ Defective U29 ✕ Defective 34 	<ul style="list-style-type: none"> ✕ Replace TUNER1. ✕ Replace IC LZ353 ✕ Replace IC 74595 ✕ Replace IC
14) DVB-T Picture no good &Sound no good	<ul style="list-style-type: none"> ✕ Defective TUNER1. ✕ DefectiveU41 	<ul style="list-style-type: none"> ✕ Replace TUNER1. ✕ Replace IC LZ353

PRINTED CIRCUIT BOARD

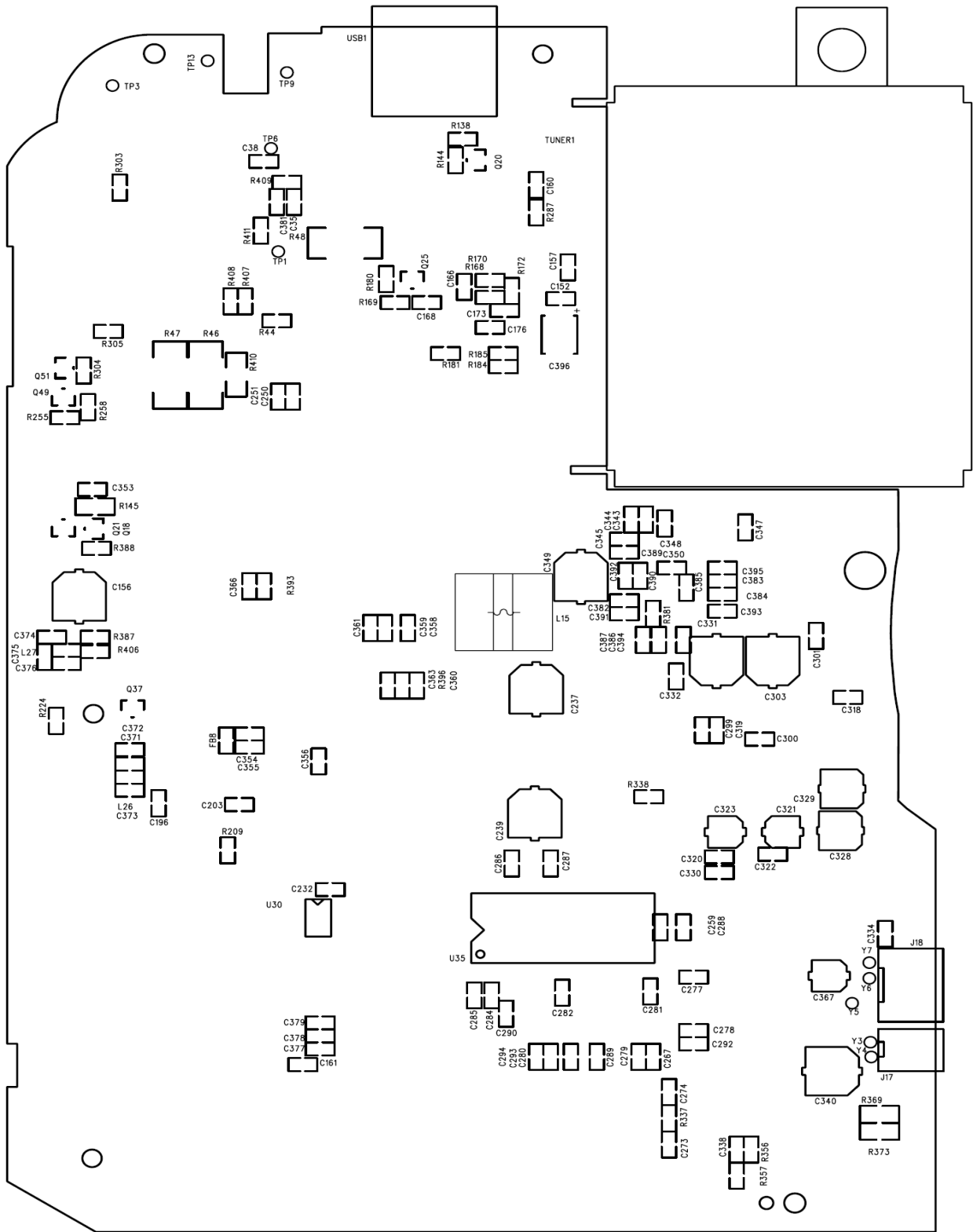
Monitor Main PCB Top View



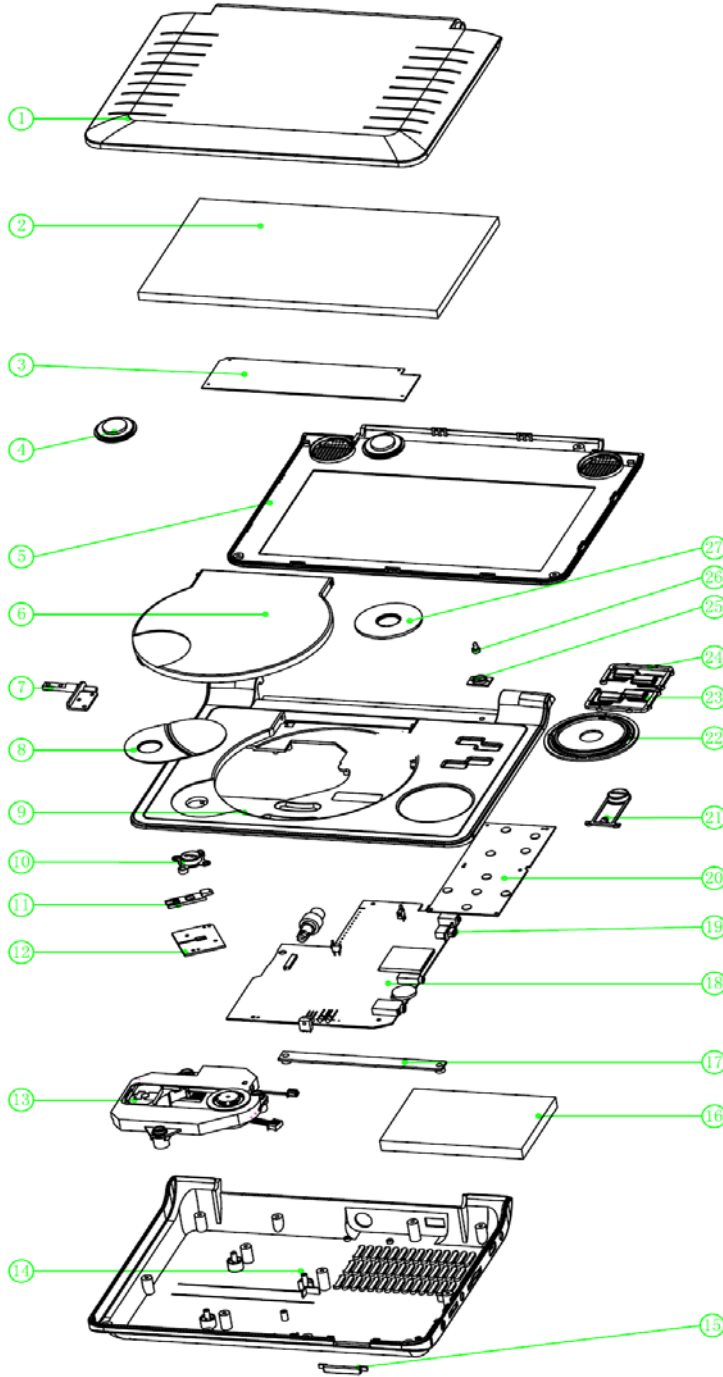
DVD Main PCB
TOP View



DVD Main PCB Bottom View



EXPLODED VIEW



27	钮装饰镜	1
26	省电钮	1
25	省电钮压片	1
24	MODE/MENU键	1
23	SETUP/PAUSE键	1
22	方向键	1
21	ENTER键	1
20	按键板	1
19	电源钮	1
18	解码板	1
17	电池压片	1
16	电池	1
15	显示接收镜	1
14	底壳	1
13	机芯	1
12	OPEN 压片	1
11	OPEN 推钮	1
10	OPEN 按钮	1
9	中壳	1
8	CD门镜	1
7	金属转轴	1
6	CD盖	1
5	翻底盖	1
4	喇叭	2
3	驱动板	1
2	显示屏	1
1	翻上盖	1
序号	零件名称	数量

恒晨电器		ISSUE	REV.3
		APPV. BY : N.X.L	DATE :
PROJECT :	DRN. BY :	SCALE :	UNIT : mm
MODEL NO. : P3920_EXPLODE	DESCRIPTION :	QTY. : 1	SHEET: 1 OF 1
DRAWING NO. :		TOLERANCE : +/- 0.10	
***** DO NOT SCALE DRAWING *****		unless otherwise specified	
MATERIAL :		FINISH :	A 4

IC BLOCK DIAGRAM & DESCRIPTION

UTC3308

UTC 3308 LINEAR INTEGRATED CIRCUIT

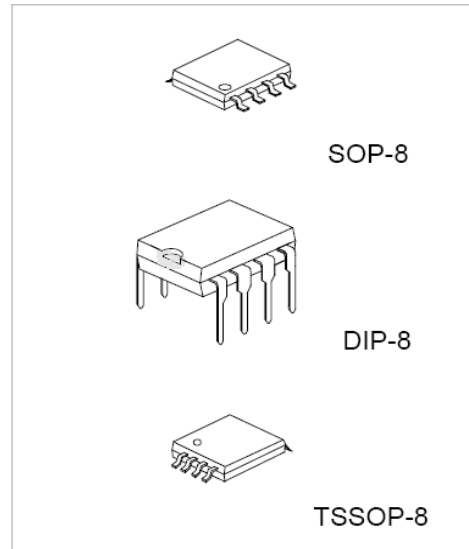
SINGLE-SUPPLY DUAL HIGH CURRENT OPERATIONAL AMPLIFIER

DESCRIPTION

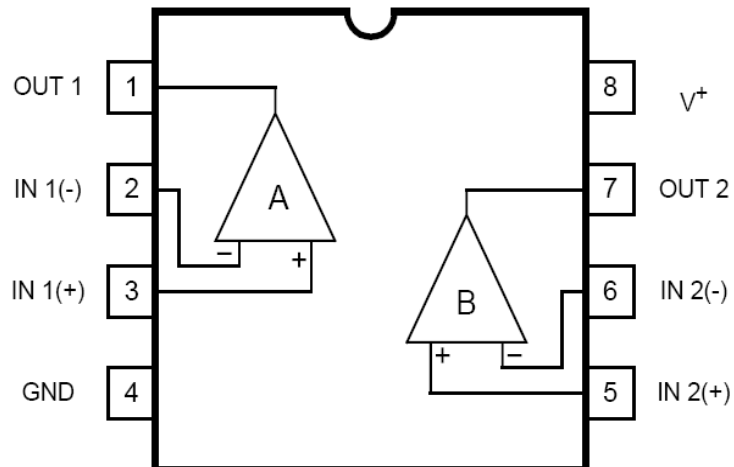
The UTC 3308 integrated circuit is a high gain, high output current, high output voltage swing dual operational amplifier capable of driving 150mA, specially for CD ROM, DVD devices.

FEATURES

- *Single Supply
- *Operating Voltage (+3V~+15V) ($\pm 1.5V \sim \pm 7.5V$)
- *High Output Current (150mA)
- *High Frequency Noise Rejection
- *Internal Enhanced Frequency Compensation



PIN CONFIGURATIONS



IC BLOCK DIAGRAM & DESCRIPTION

UTC3308 LINEAR INTEGRATED CIRCUIT

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V+	15V / ±7.5V	V
Differential Input Voltage	V _{ID}	15	V
Input Voltage	V _I	-0.3 ~ +15	V
Power Dissipation	P _D	300	mW
Operating Temperature	T _{opr}	-20 to +75	°C
Storage Temperature	T _{stg}	-40 to +125	°C

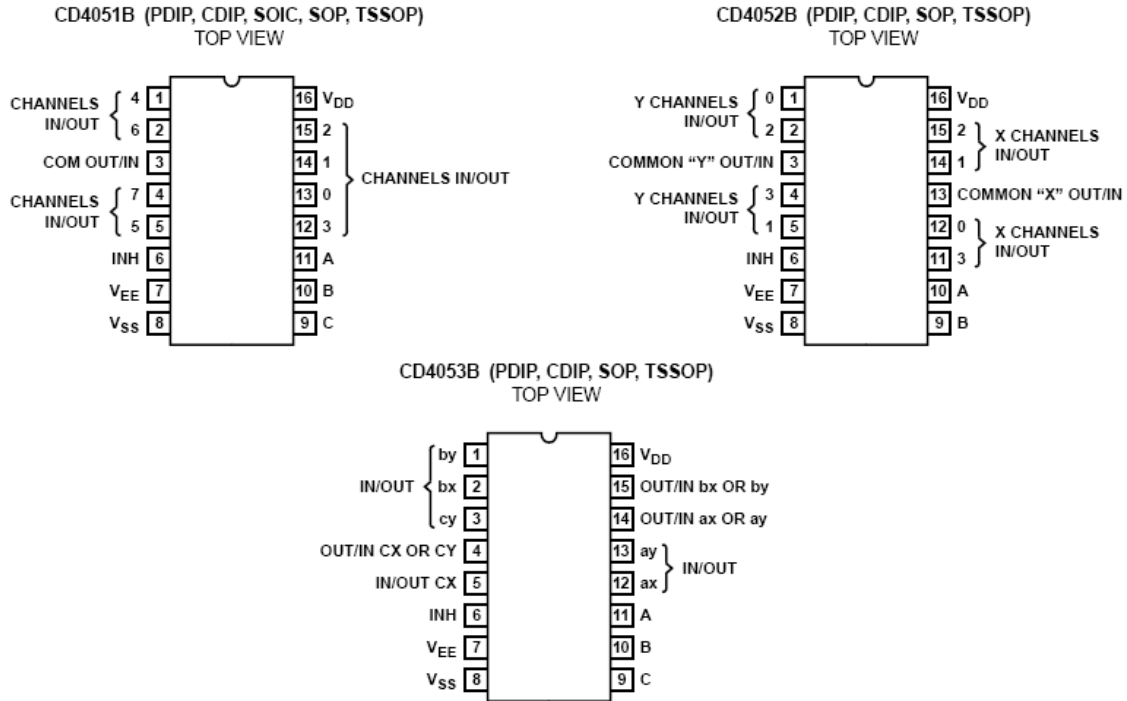
ELECTRICAL CHARACTERISTICS (Ta=25°C, V⁺=5V)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Offset Voltage	V _{IO}	R _s =0Ω		2	5	mV
Input Offset Current	I _{IO}			5	100	nA
Input Bias Current	I _b			100	500	nA
Large Signal Voltage Gain	A _v	R _L =2kΩ	88	100		dB
Input Common Voltage Range	V _{ICM}		V ⁺ -2			V
Maximum Output Voltage Swing 1	V _{OM1}	R _L ≥2kΩ	3.5			V
Maximum Output Voltage Swing 2	V _{OM2}	I _o =70mA	3.2			V
Common Mode Rejection Ratio	CMR		80	90		dB
Supply Voltage Rejection Ratio	SVR		80	90		dB
Operating Current	I _{cc}	R _L =∞	3	4	5	mA
Slew Rate	SR			1.0		V/μs
Unity Gain Bandwidth	GB			1.3		MHz
Operating Voltage Range	V ⁺				15	V

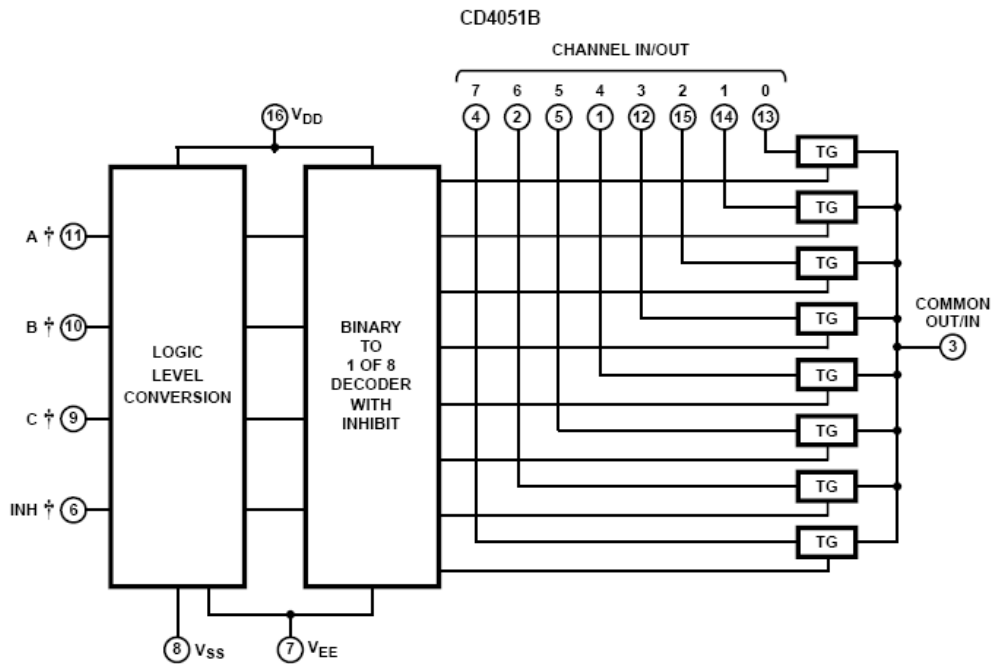
IC BLOCK DIAGRAM & DESCRIPTION

CD4053

Pinouts



Functional Block Diagrams

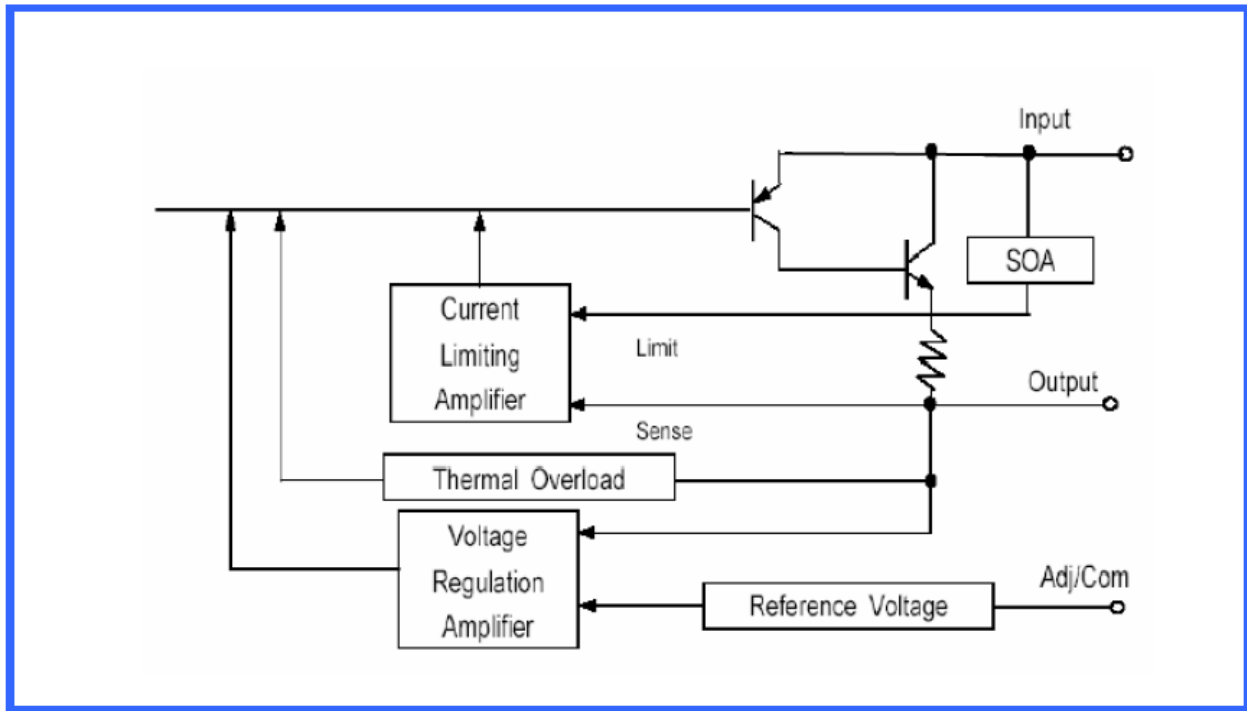


† All inputs are protected by standard CMOS protection network.

IC BLOCK DIAGRAM & DESCRIPTION

ASM1117

Block Diagram



Application Information

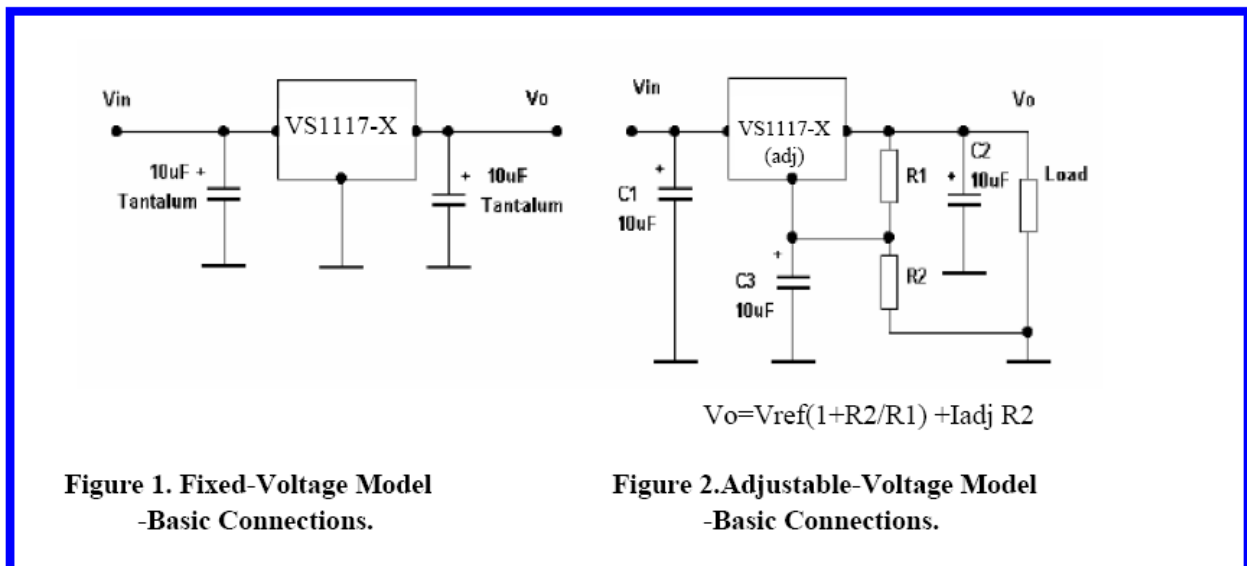


Figure 1. Fixed-Voltage Model -Basic Connections.

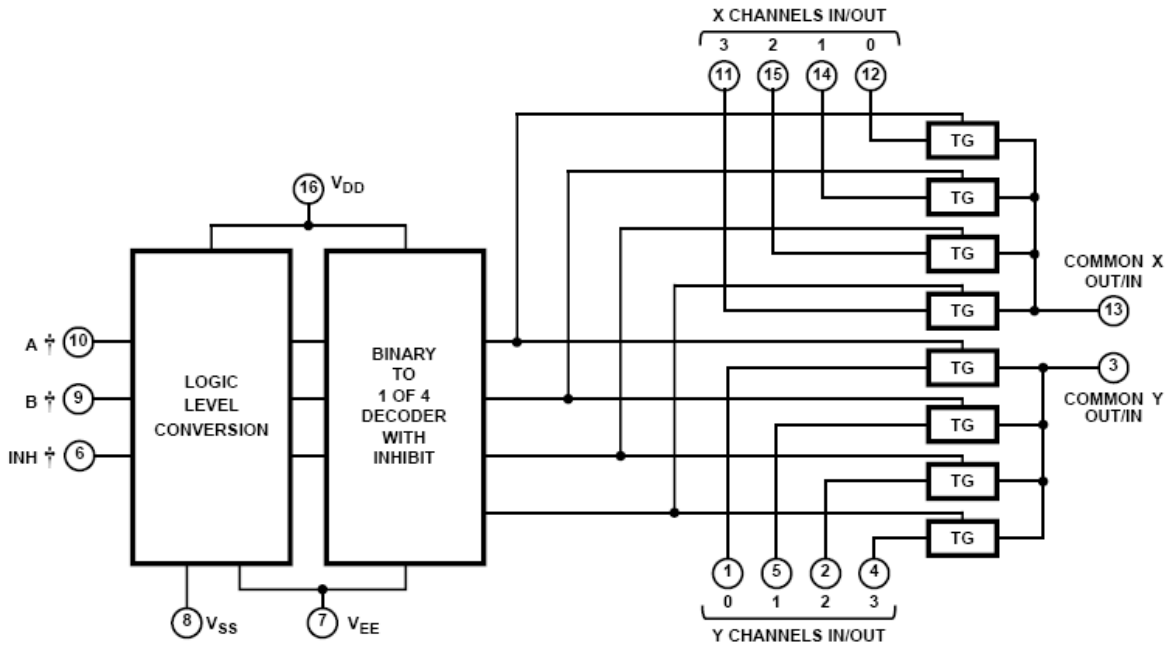
Figure 2. Adjustable-Voltage Model -Basic Connections.

IC BLOCK DIAGRAM & DESCRIPTION

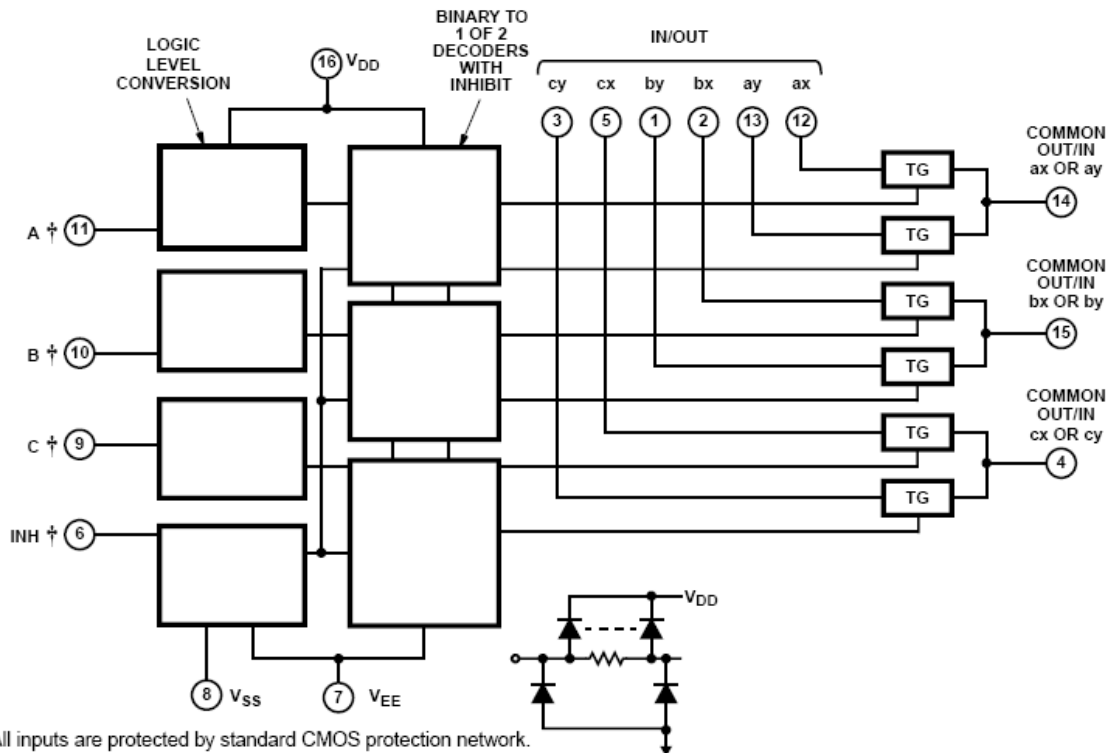
CD4053

Functional Block Diagrams (Continued)

CD4052B



CD4053B



† All inputs are protected by standard CMOS protection network.

IC BLOCK DIAGRAM & DESCRIPTION

CD4053

Test Circuits and Waveforms (Continued)

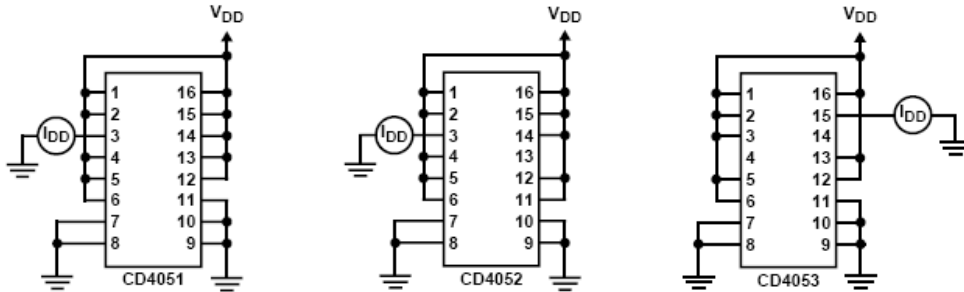


FIGURE 13. OFF CHANNEL LEAKAGE CURRENT - ALL CHANNELS OFF

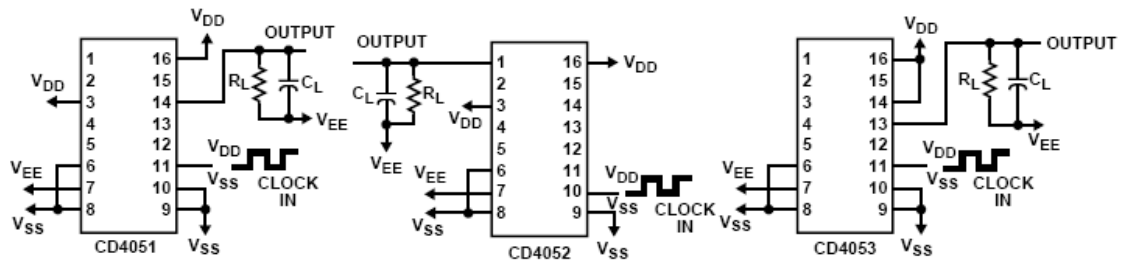


FIGURE 14. PROPAGATION DELAY - ADDRESS INPUT TO SIGNAL OUTPUT

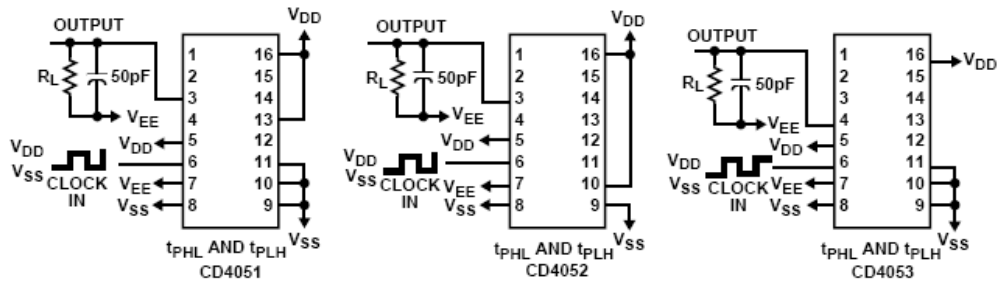


FIGURE 15. PROPAGATION DELAY - INHIBIT INPUT TO SIGNAL OUTPUT

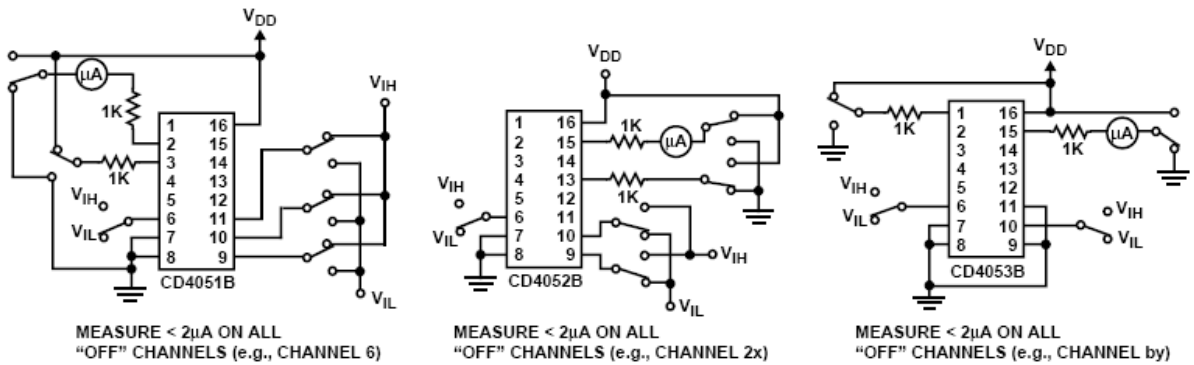
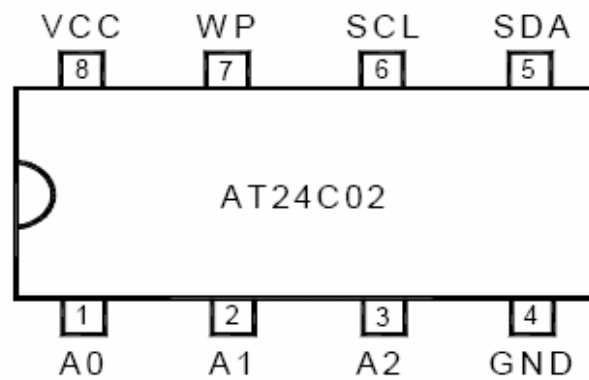
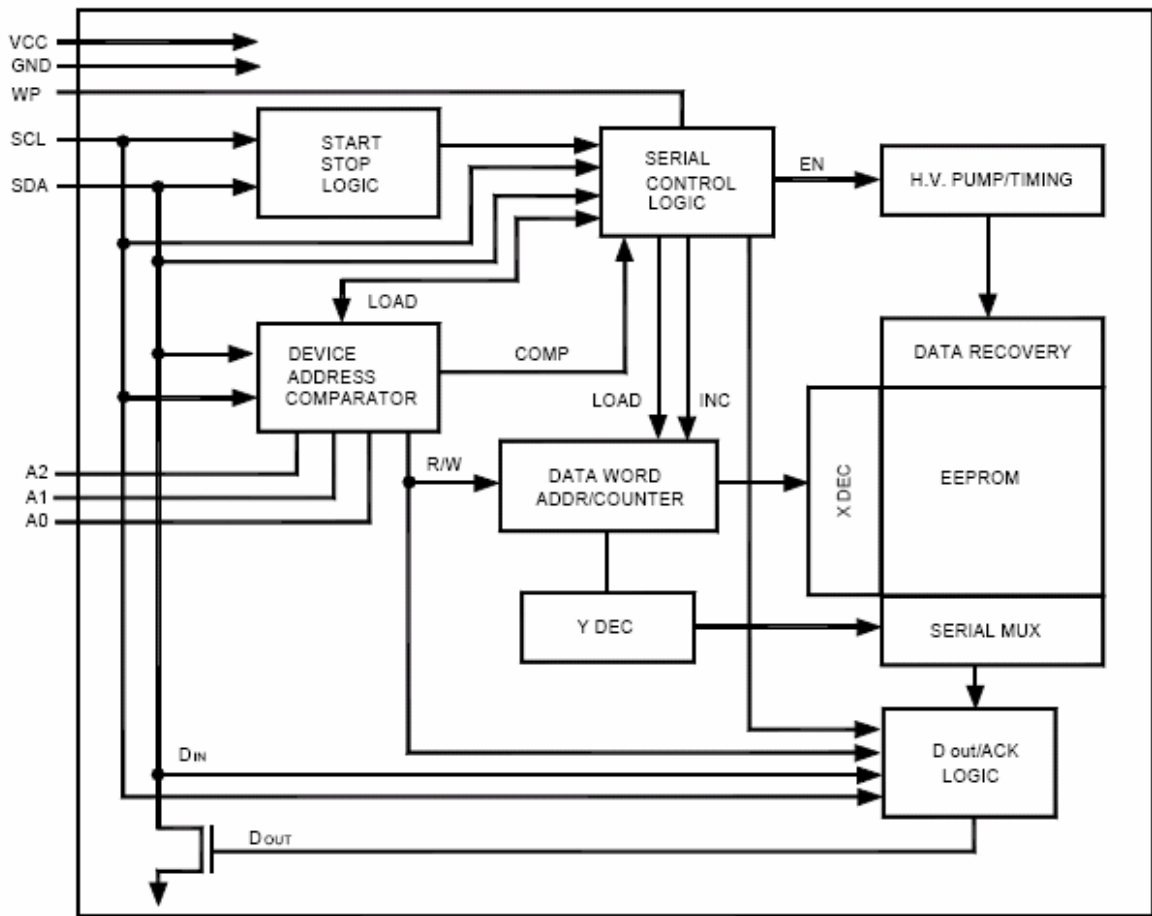


FIGURE 16. INPUT VOLTAGE TEST CIRCUITS (NOISE IMMUNITY)

IC BLOCK DIAGRAM & DESCRIPTION

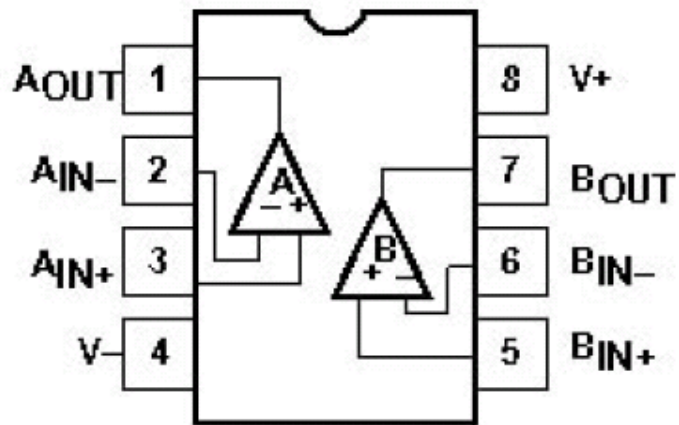
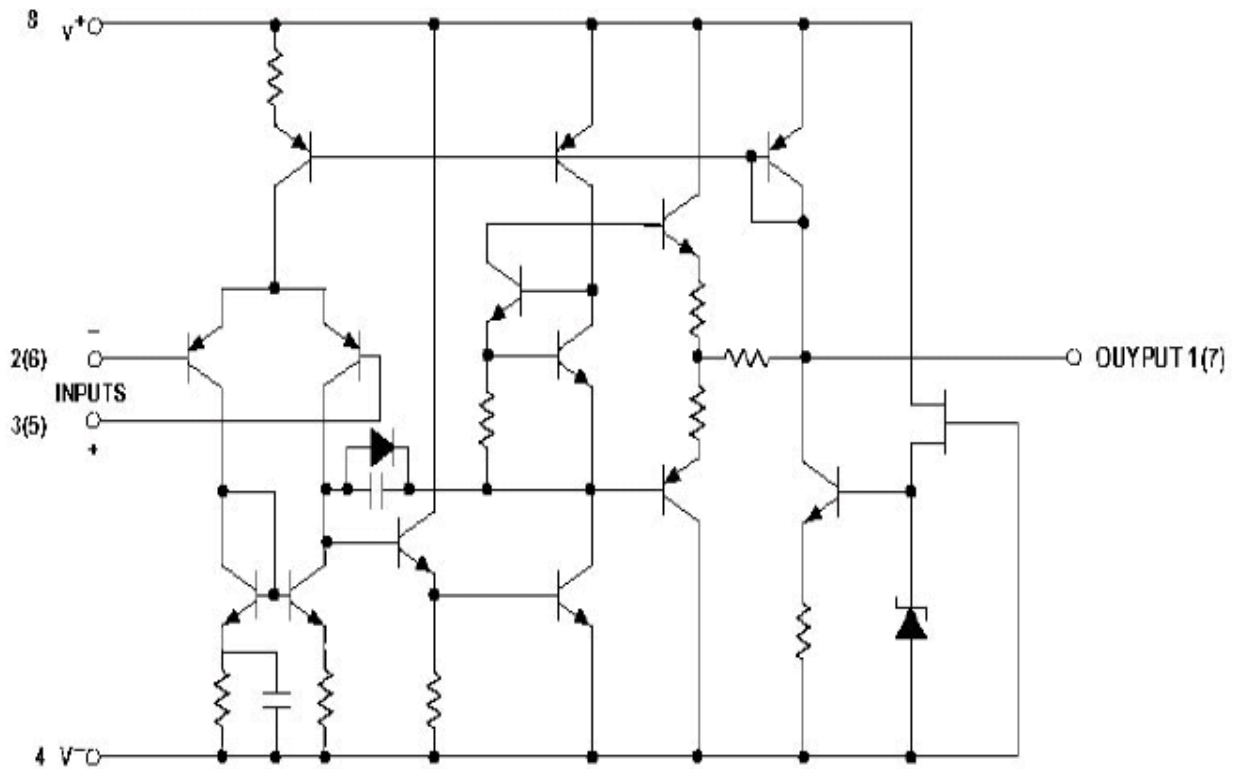
IC AT24C02 (DVD)



- | | | | |
|----|-----|----|-----|
| 1. | A0 | 5. | SDA |
| 2. | A1 | 6. | SCL |
| 3. | A2 | 7. | WP |
| 4. | GND | 8. | VCC |

IC BLOCK DIAGRAM & DESCRIPTION

IC C4558



PIN	NAME	PIN	NAME
1.	AOUT	5.	BIN+
2.	AIN-	6.	BIN-
3.	AIN+	7.	BOUT
4.	V-	8.	V+

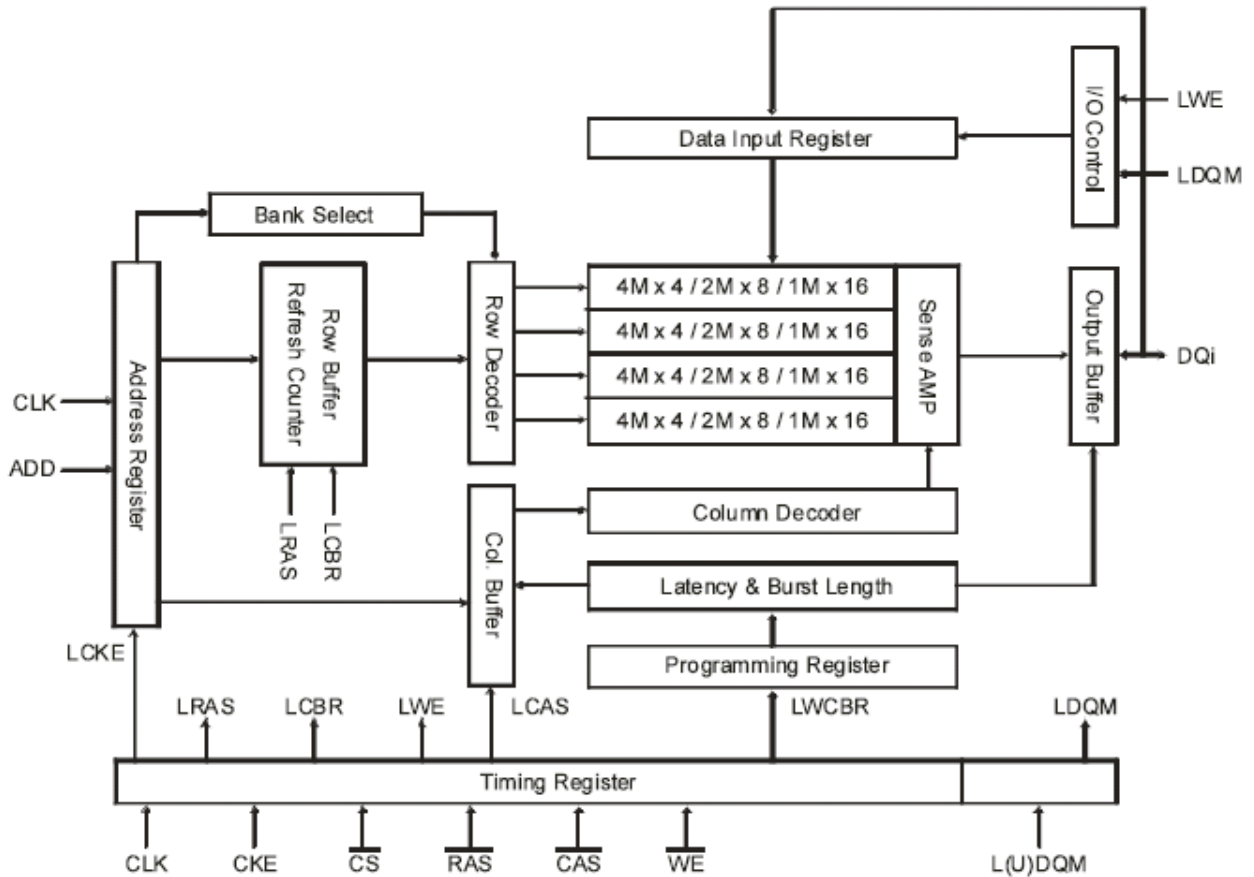
IC BLOCK DIAGRAM & DESCRIPTION

IC BA5954

Pin No.	Pin name	Function
1.	VINFC	Focus drive input
2.	CFC err1	For connection of capacitor for the error amp filter
3.	CFC err2	For connection of capacitor for the error amp filter
4.	VINSL +	Op-amp input (+) for the sled driver
5.	VINSL -	Op-amp input (-) for the sled driver
6.	VOSSL	Op-amp output for the sled driver
7.	VNFFC	Focus driver feedback pin
8.	Vcc	Vcc
9.	PV cc1	Power Vcc for sled driver block
10.	PGND	Ground for Sled Driver block
11.	VOSSL -	Sled driver output (-)
12.	VOSSL +	Sled driver output (+)
13.	VOFC -	Focus driver output (-)
14.	VOFC +	Focus driver output (+)
15.	VOTK +	Tracking driver output (+)
16.	VOTK -	Tracking driver output (-)
17.	VOLD +	Loading driver output (+)
18.	VOLD -	Loading driver output (-)
19.	PGND	Ground for Actuator driver block
20.	VNFTK	Tracking driver feedback pin
21.	PV cc2	Power Vcc for Actuator driver block
22.	GND	Ground
23.	VINTK	Loading driver input
24.	CTKerr2	For connection of capacitor for the error amp filter
25.	CTKerr1	For connection of capacitor for the error amp filter
26.	VINTK	Tracking driver input
27.	BLAS	Bias input
28.	STBY	Stand – By control

IC BLOCK DIAGRAM & DESCRIPTION

IC K4S641632



x16	x8	x4			x4	x8	x16
V _{DD}	V _{DD}	V _{DD}	1	54	V _{SS}	V _{SS}	V _{SS}
DQ0	DQ0	N.C	2	53	N.C	DQ7	DQ15
V _{DDQ}	V _{DDQ}	V _{DDQ}	3	52	V _{SSQ}	V _{SSQ}	V _{SSQ}
DQ1	N.C	N.C	4	51	N.C	N.C	DQ14
DQ2	DQ1	DQ0	5	50	DQ3	DQ6	DQ13
V _{SSQ}	V _{SSQ}	V _{SSQ}	6	49	V _{DDQ}	V _{DDQ}	V _{DDQ}
DQ3	N.C	N.C	7	48	N.C	N.C	DQ12
DQ4	DQ2	N.C	8	47	N.C	DQ5	DQ11
V _{DDQ}	V _{DDQ}	V _{DDQ}	9	46	V _{SSQ}	V _{SSQ}	V _{SSQ}
DQ5	N.C	N.C	10	45	N.C	N.C	DQ10
DQ6	DQ3	DQ1	11	44	DQ2	DQ4	DQ9
V _{SSQ}	V _{SSQ}	V _{SSQ}	12	43	V _{DDQ}	V _{DDQ}	V _{DDQ}
DQ7	N.C	N.C	13	42	N.C	N.C	DQ8
V _{DD}	V _{DD}	V _{DD}	14	41	V _{SS}	V _{SS}	V _{SS}
<u>LDQM</u>	<u>N.C</u>	<u>N.C</u>	15	40	N.C/RFU	N.C/RFU	N.C/RFU
<u>WE</u>	<u>WE</u>	<u>WE</u>	16	39	DQM	DQM	UDQM
<u>CAS</u>	<u>CAS</u>	<u>CAS</u>	17	38	CLK	CLK	CLK
<u>RAS</u>	<u>RAS</u>	<u>RAS</u>	18	37	CKE	CKE	CKE
CS	CS	CS	19	36	N.C	N.C	N.C
BA0	BA0	BA0	20	35	A11	A11	A11
BA1	BA1	BA1	21	34	A9	A9	A9
A10/AP	A10/AP	A10/AP	22	33	A8	A8	A8
A0	A0	A0	23	32	A7	A7	A7
A1	A1	A1	24	31	A6	A6	A6
A2	A2	A2	25	30	A5	A5	A5
A3	A3	A3	26	29	A4	A4	A4
V _{DD}	V _{DD}	V _{DD}	27	28	V _{SS}	V _{SS}	V _{SS}

54Pin TSOP (II)
(400mil x 875mil)
(0.8 mm Pin pitch)

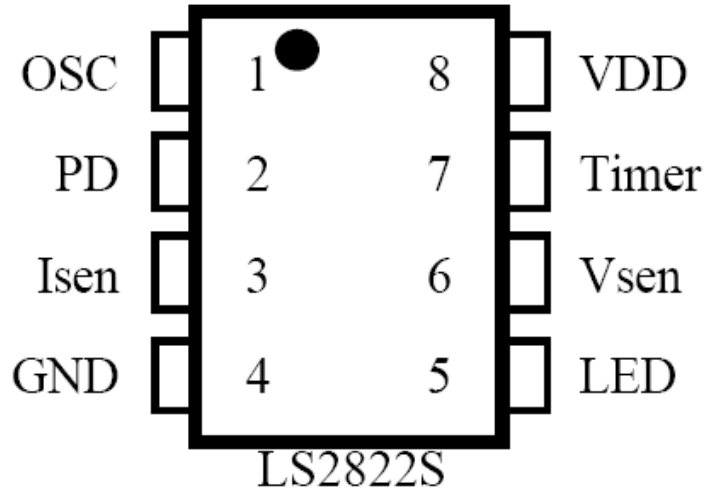
IC BLOCK DIAGRAM & DESCRIPTION

IC K4S641632

Pin	Name	Input Function
CLK	System clock	Active on the positive going edge to sample all inputs.
$\overline{\text{CS}}$	Chip select	Disables or enables device operation by masking or enabling all inputs except CLK, CKE and DQM
CKE	Clock enable	Masks system clock to freeze operation from the next clock cycle. CKE should be enabled at least one cycle prior to new command. Disable input buffers for power down in standby.
A ₀ ~ A ₁₁	Address	Row/column addresses are multiplexed on the same pins. Row address : RA ₀ ~ RA ₁₁ , Column address : (x4 : CA ₀ ~ CA ₉ , x8 : CA ₀ ~ CA ₈ , x16 : CA ₀ ~ CA ₇)
BA ₀ ~ BA ₁	Bank select address	Selects bank to be activated during row address latch time. Selects bank for read/write during column address latch time.
$\overline{\text{RAS}}$	Row address strobe	Latches row addresses on the positive going edge of the CLK with $\overline{\text{RAS}}$ low. Enables row access & precharge.
$\overline{\text{CAS}}$	Column address strobe	Latches column addresses on the positive going edge of the CLK with $\overline{\text{CAS}}$ low. Enables column access.
$\overline{\text{WE}}$	Write enable	Enables write operation and row precharge. Latches data in starting from CAS, WE active.
DQM	Data input/output mask	Makes data output Hi-Z, t _{SHZ} after the clock and masks the output. Blocks data input when DQM active.
DQ ₀ ~ x ₁₅	Data input/output	Data inputs/outputs are multiplexed on the same pins.
V _{DD} /V _{SS}	Power supply/ground	Power and ground for the input buffers and the core logic.
V _{DDQ} /V _{SSQ}	Data output power/ground	Isolated power supply and ground for the output buffers to provide improved noise immunity.
N.C/RFU	No connection /reserved for future use	This pin is recommended to be left No Connection on the device.

IC BLOCK DIAGRAM & DESCRIPTION

IC LS2822

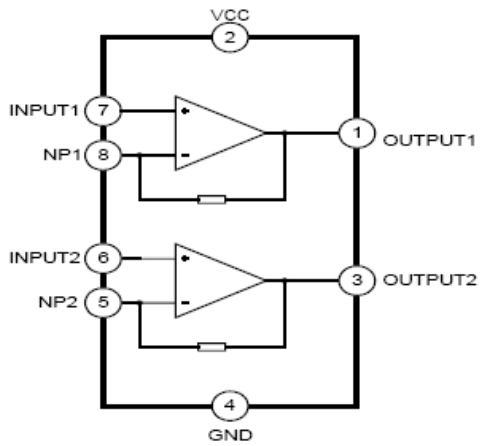


PIN#	PIN NAME	TYPE	DESCRIPTION
1	OSC	I	RC Oscillate Input
2	PD	O	Charge Control Output
3	Isen	I	Charge Current Sense Input
4	GND	P	Ground
5	LED	O	LED Status Output
6	Vsen	I	Charge Voltage Sense Input
7	Timer	I	Timer
8	VDD	P	5 Volt Power

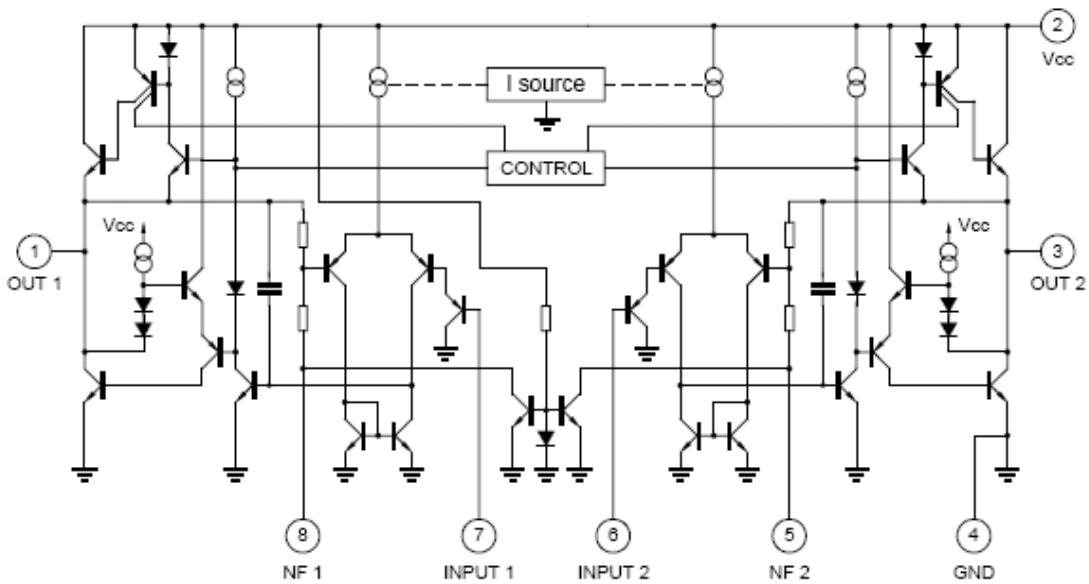
IC BLOCK DIAGRAM & DESCRIPTION

IC TDA2822

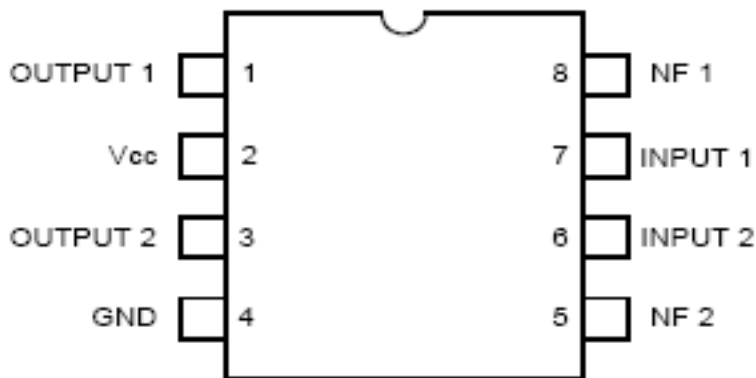
BLOCK DIAGRAM



SCHEMATIC DIAGRAM

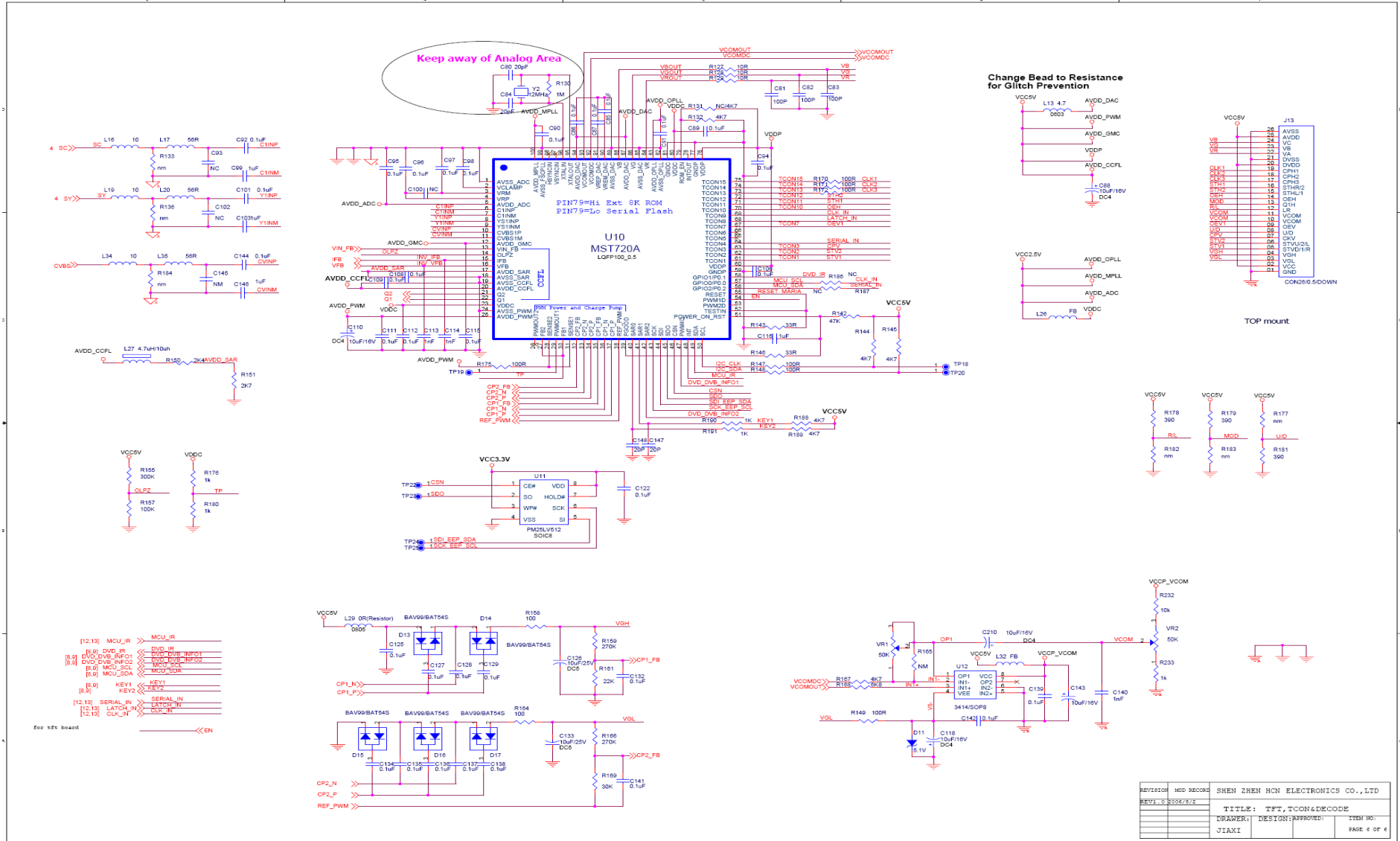


PIN CONFIGURATIONS



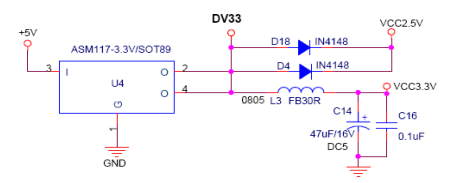
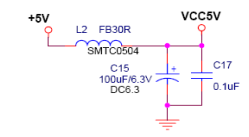
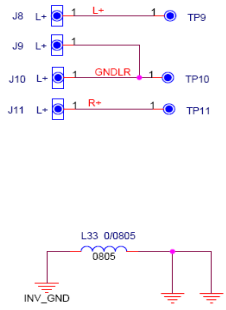
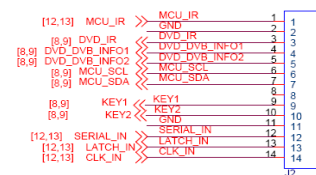
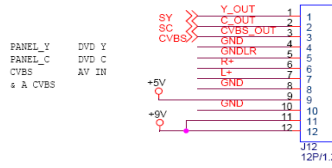
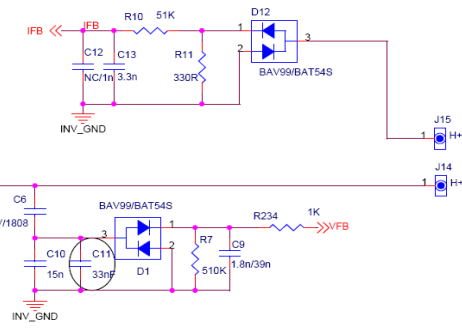
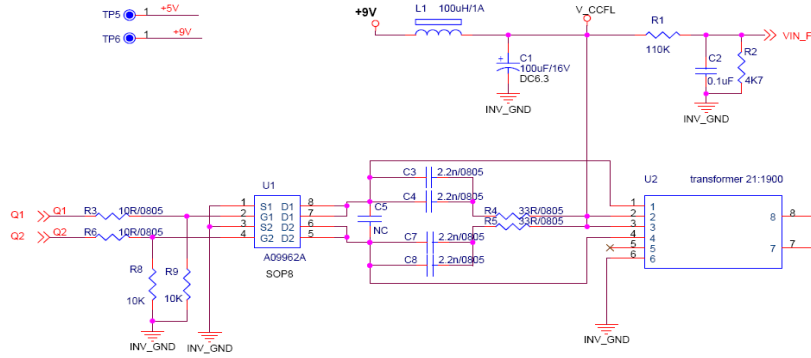
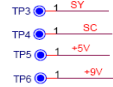
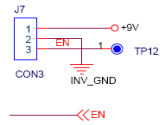
SCHEMATIC DIAGRAMS

Monitor Part



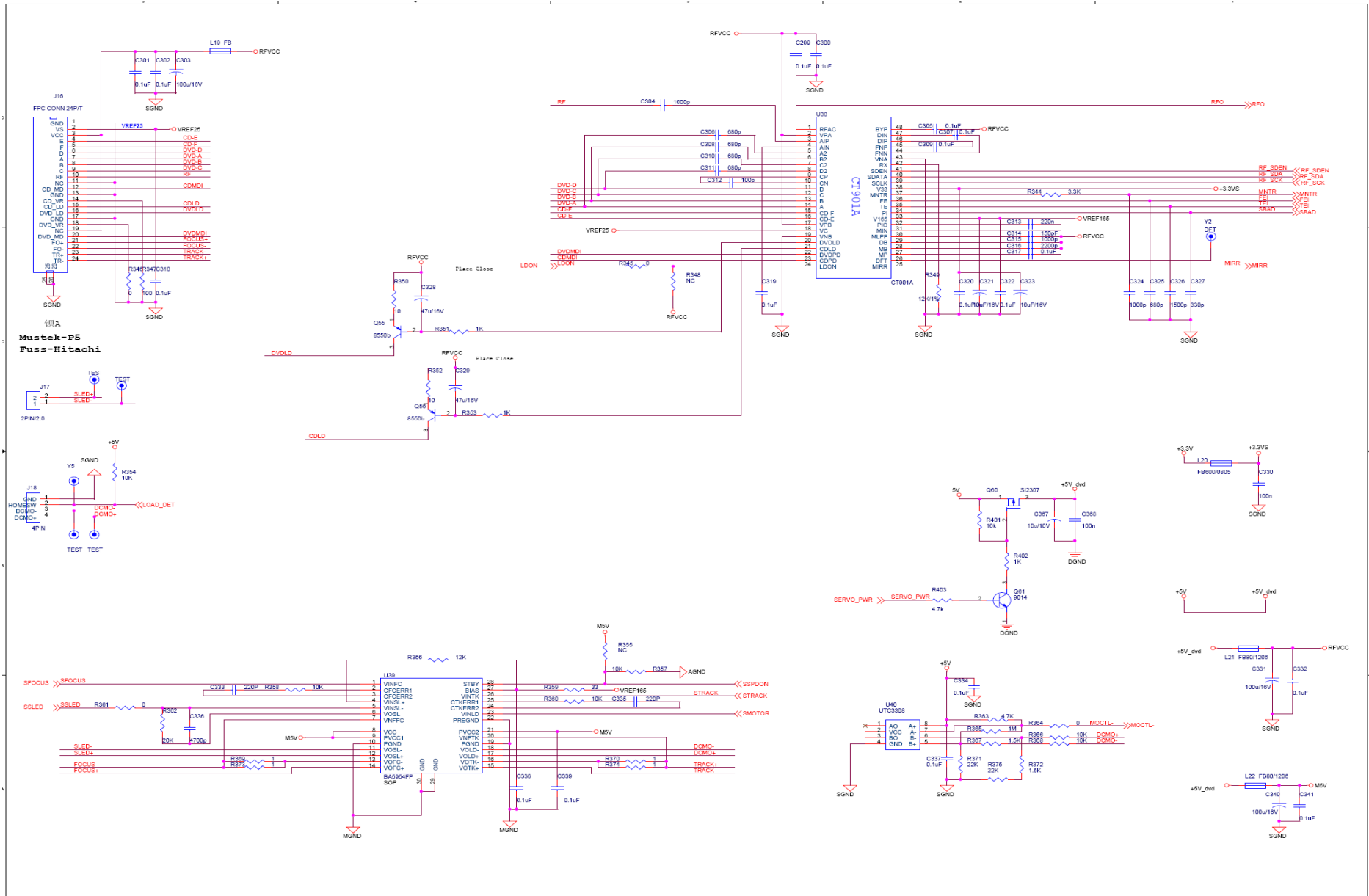
REV:1010	MOD:RECORD	SHEN ZHEN HCN ELECTRONICS CO.,LTD
REV:1.0	3/24/2012	TITLE: TFT_TCON&DECODE
		DRAWER: DESIGN/ APPROVED: ITEXT/NO:
		JIAOXI
		PAGE 4 of 6

Monitor Part

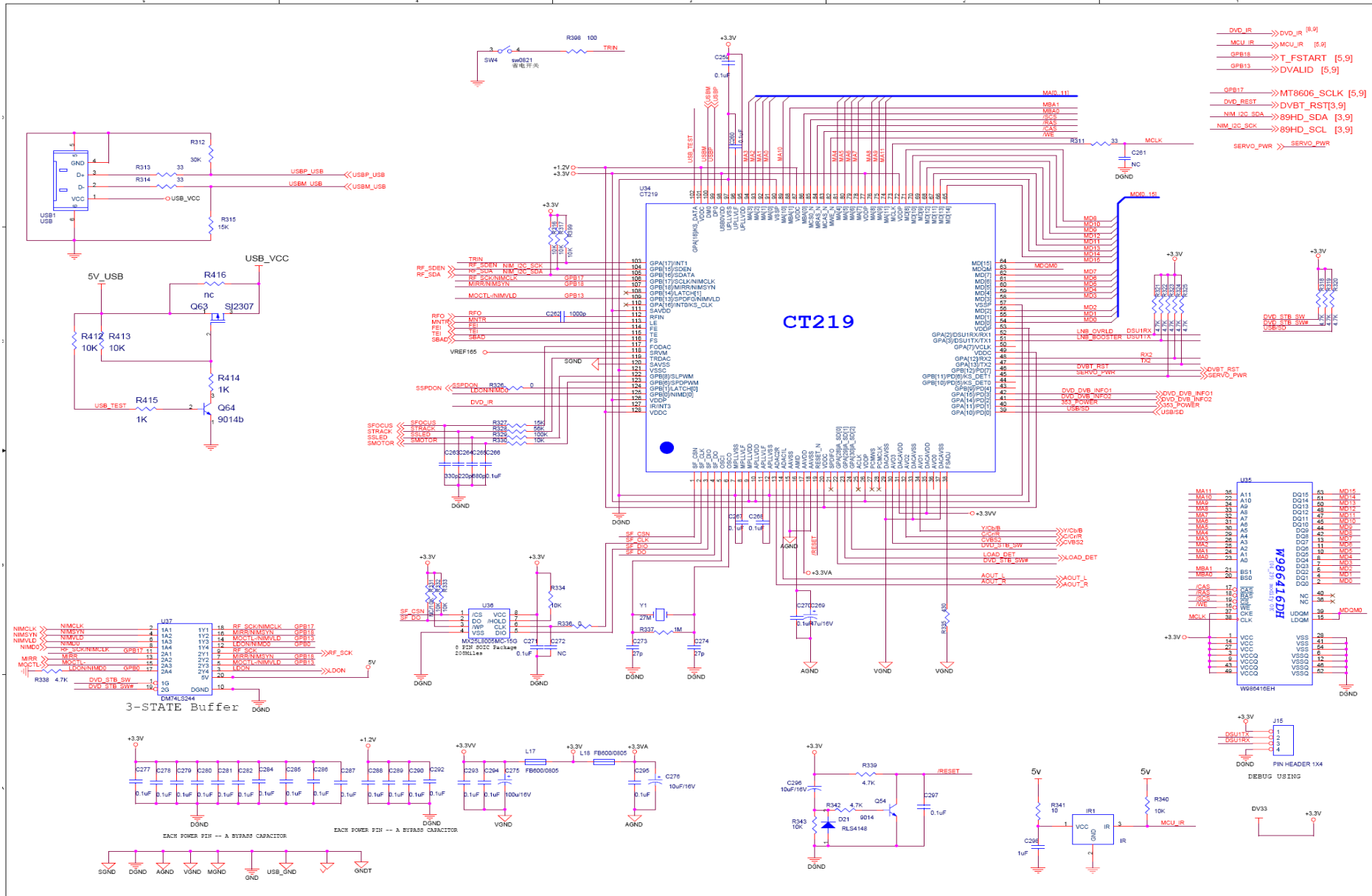


REVISION	MOD RECORD	SHEN ZHEN HCN ELECTRONICS CO.,LTD	
REV1.0	2006/8/2	TITLE: FM'OUT&INVENTER	
		DRAWER:	ITEM NO:
		JIAXI	PAGE 5 OF 6

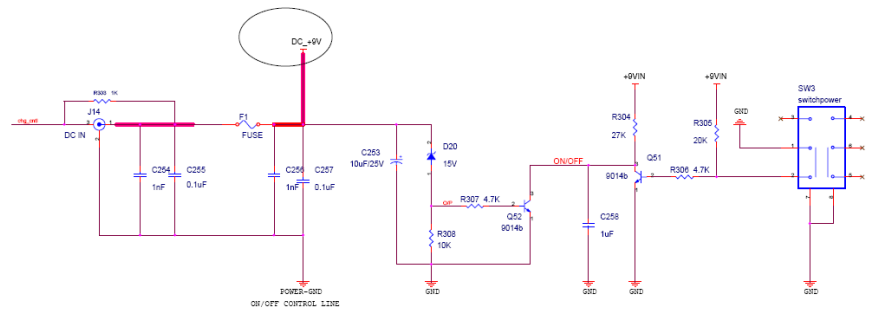
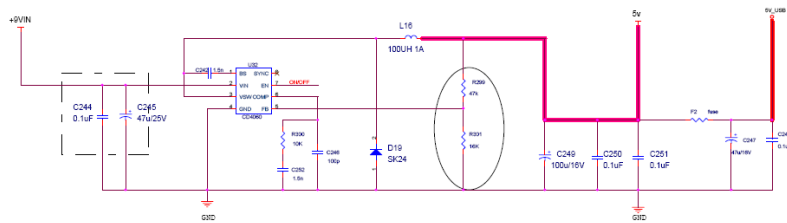
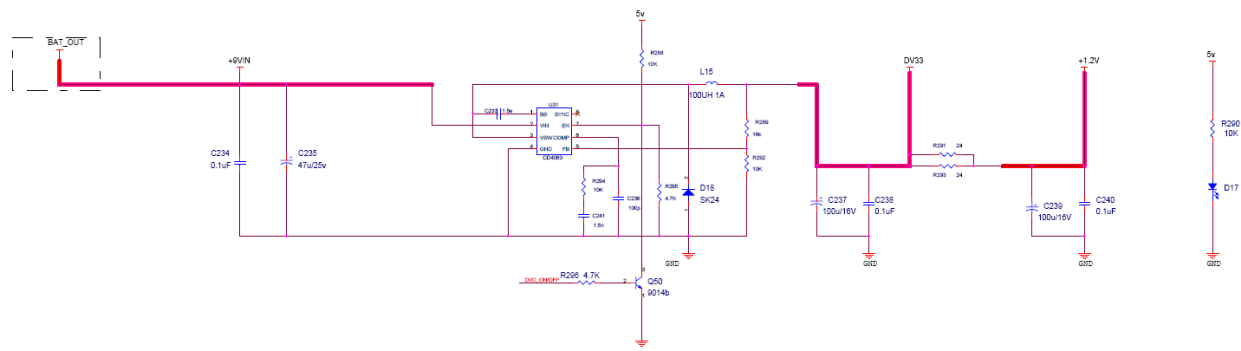
DVD Part



DVD Part



DVD Part

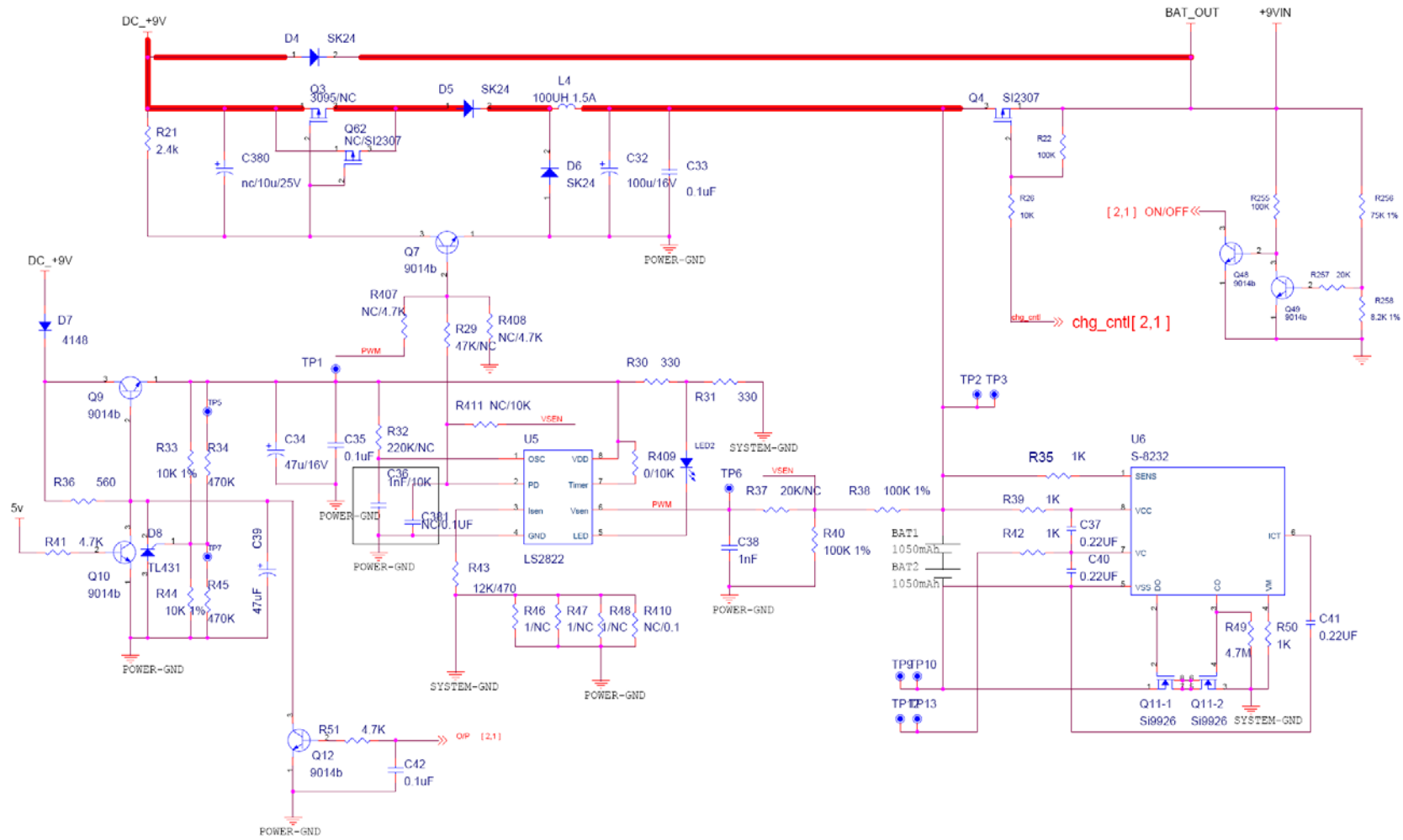


↑
THE ON/OFF CONTROL
LINE IS INCLUDED BY
DC JACK

— ON/OFF —> ON/OFF [2.1]
— O/P —> O/P [2.1]
— chg_cntl —> chg_cntl [2.1]

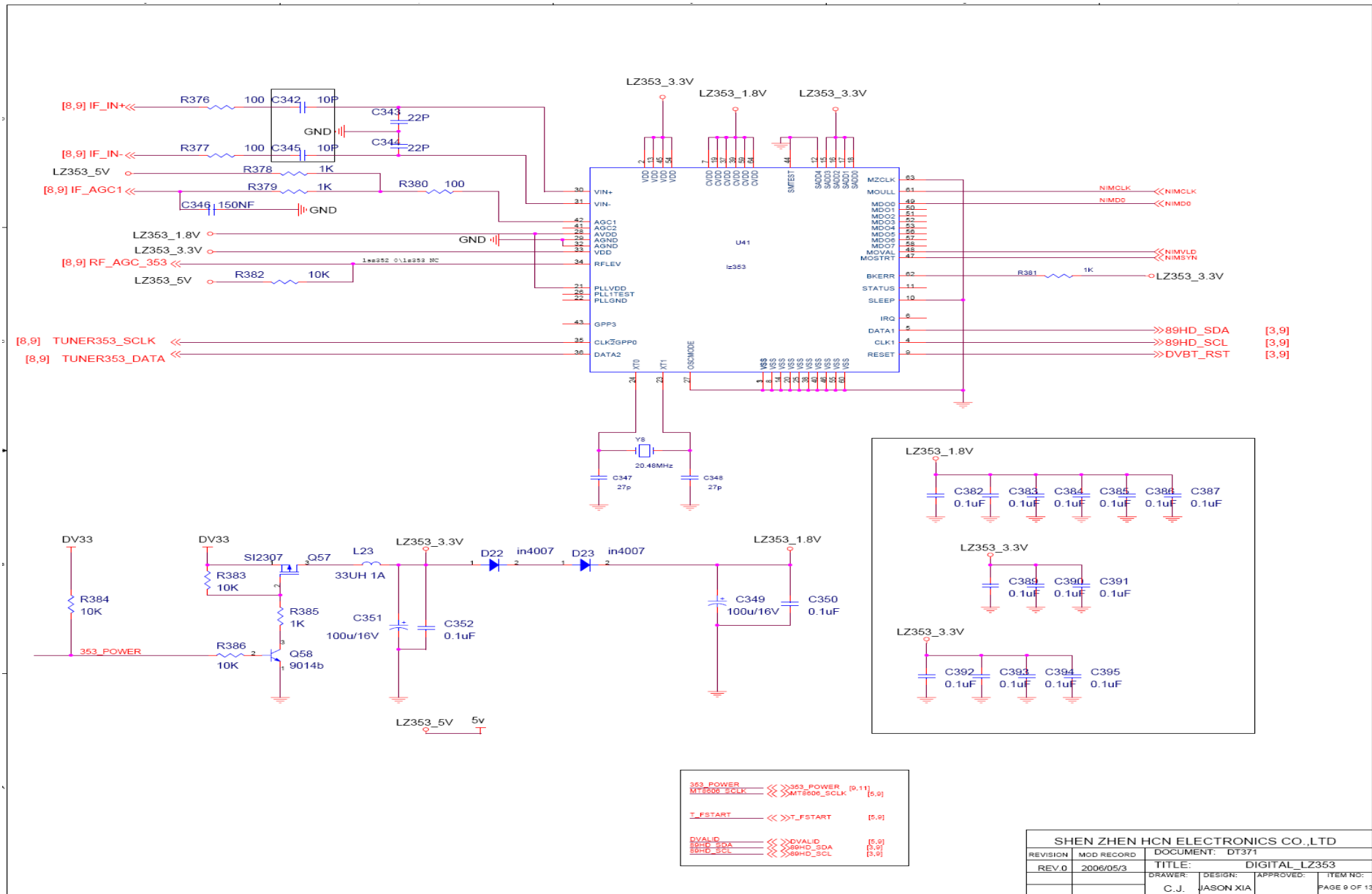
SHEN ZHEN HON ELECTRONICS CO.,LTD			
REV/ID	MOD/RECORD	DOCUMENT	DT/371
REV/0	2005/04/01	TITLE:	POWER
	DESIGNER:	DESIGNER:	DESIGNER:
	C.J.	Jason Xia	1
			PAGE 1 OF 11

DVD Part



SHEN ZHEN HCN ELECTRONICS CO.,LTD				
REVISION	MOD RECORD	DOCUMENT	PD12920	
REV_0	2006/08/12	TITLE:	CHARGE	
		DRAWER:	DESIGN:	APPROVED:
			xiliang	
				ITEM NO:
				PAGE 2 OF 9

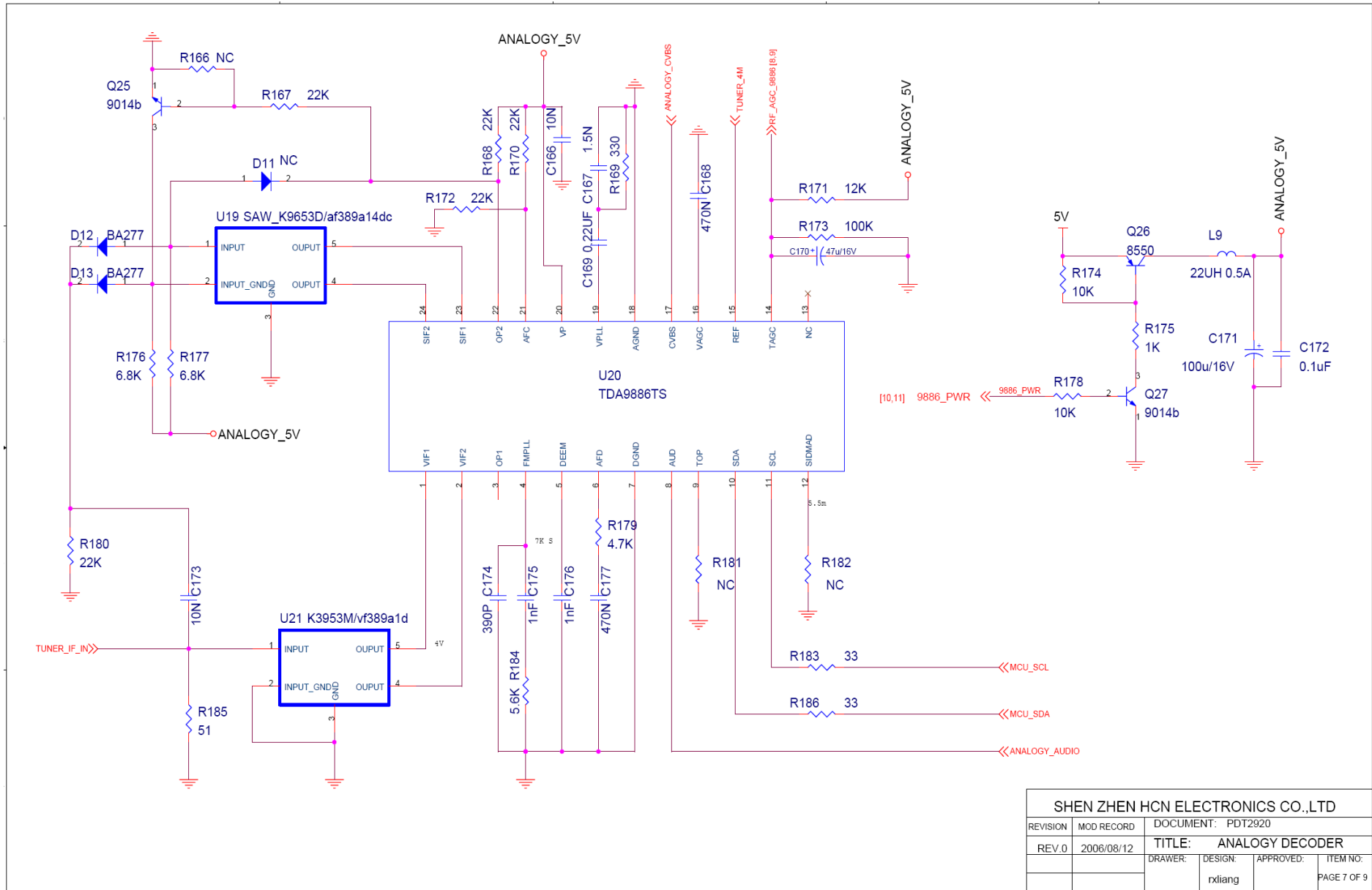
DVD Part



353_POWER	<<>>	353_POWER	[0,11]
MTF800_SCLK	<<>>	MTF800_SCLK	[5,9]
T_FSTART	<<>>	T_FSTART	[5,9]
DVALID	<<>>	DVALID	[5,9]
89HD_SDA	<<>>	89HD_SDA	[3,9]
89HD_SCL	<<>>	89HD_SCL	[3,9]

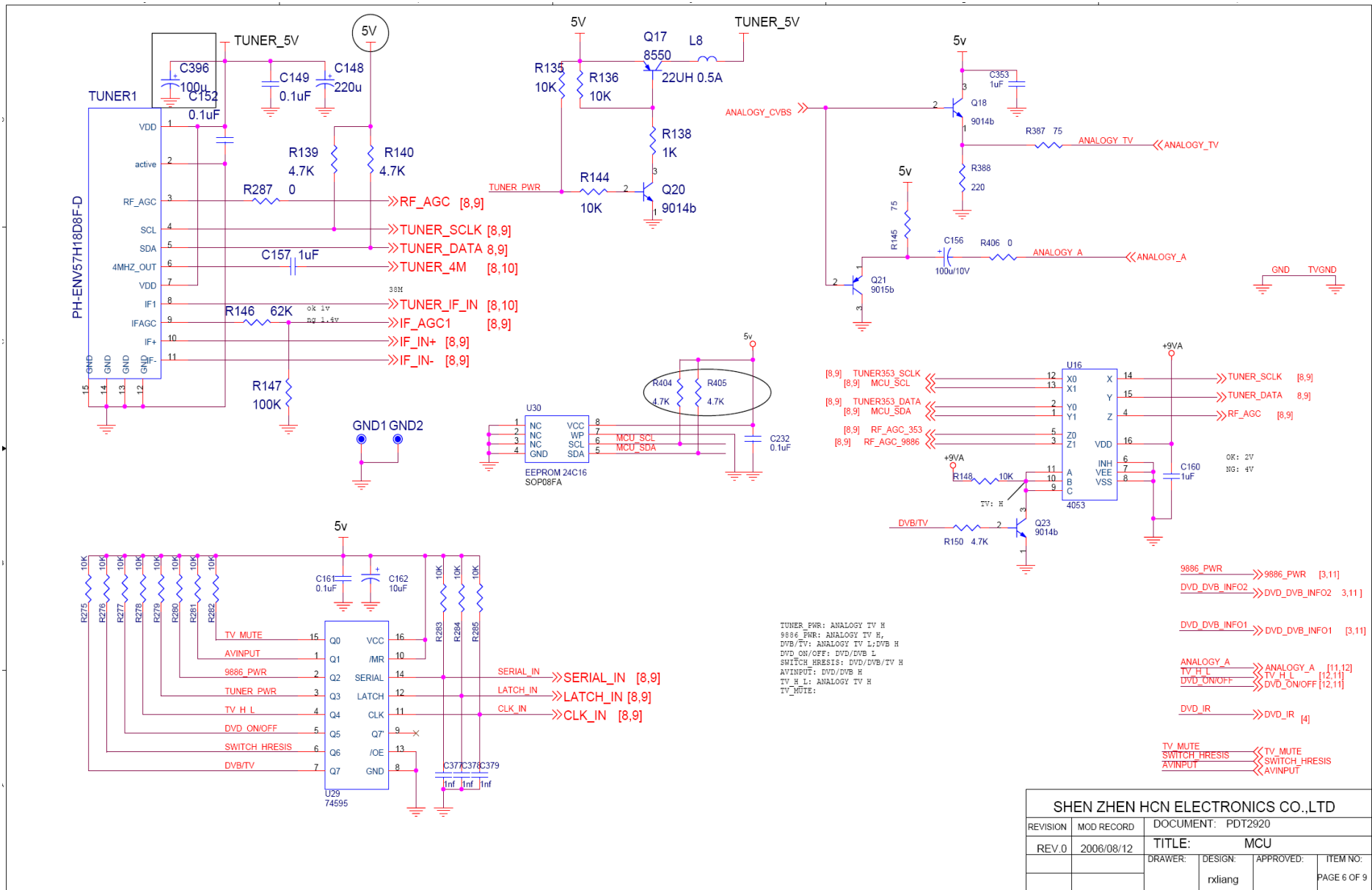
SHEN ZHEN HCN ELECTRONICS CO.,LTD			
REVISION	MOD RECORD	DOCUMENT: DT371	
REV 0	2006/05/3	TITLE:	DIGITAL_LZ353
		DRAWER:	C.J.
		DESIGNER:	JASON XIA
		APPROVED:	
		ITEM NO:	
		PAGE 9 OF 15	

DVD Part



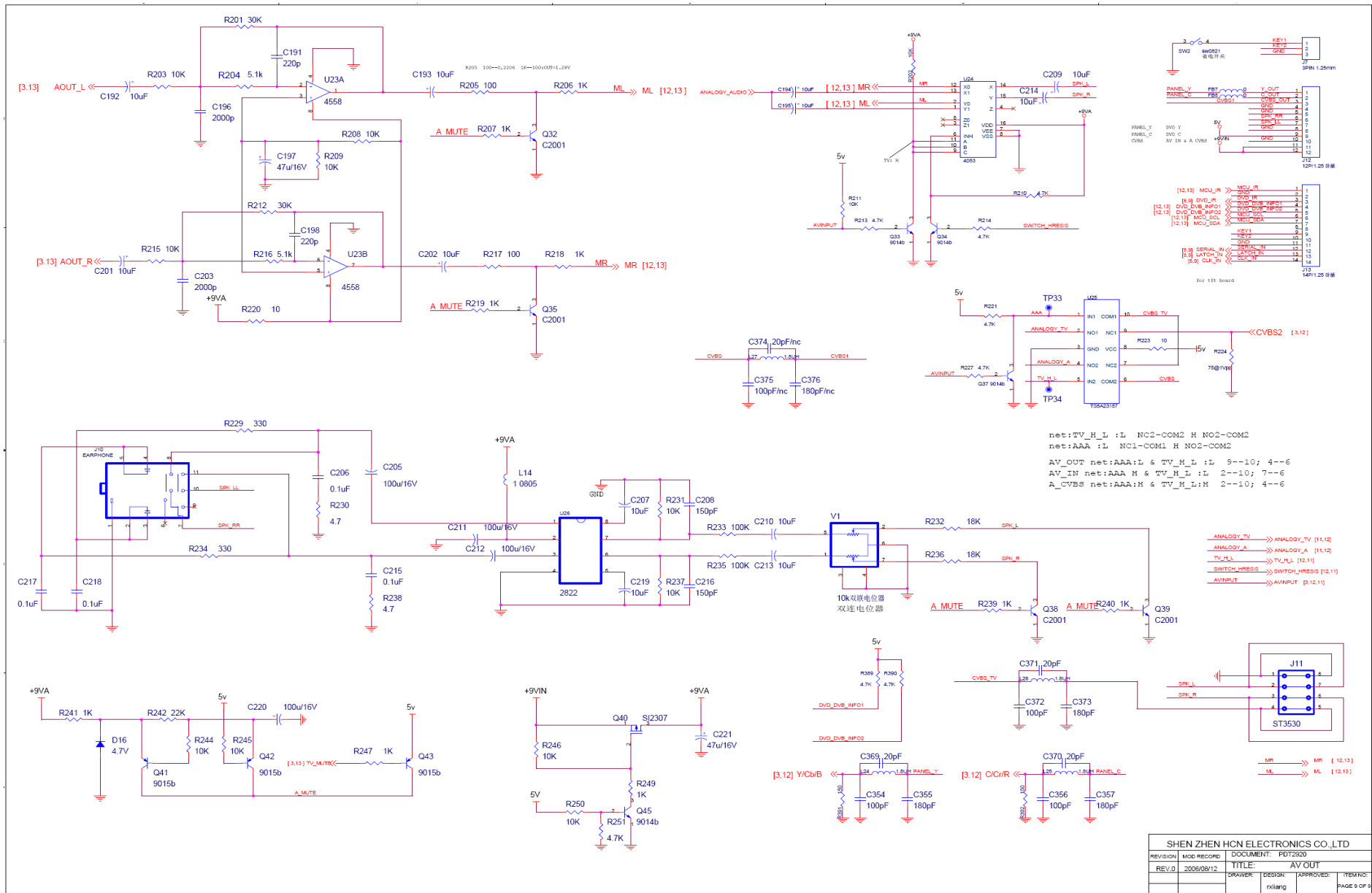
SHEN ZHEN HCN ELECTRONICS CO.,LTD					
REVISION	MOD RECORD	DOCUMENT: PDT2920			
REV.0	2006/08/12	TITLE: ANALOGY DECODER			
		DRAWER:	DESIGN:	APPROVED:	ITEM NO:
			rxliang		PAGE 7 OF 9

DVD Part

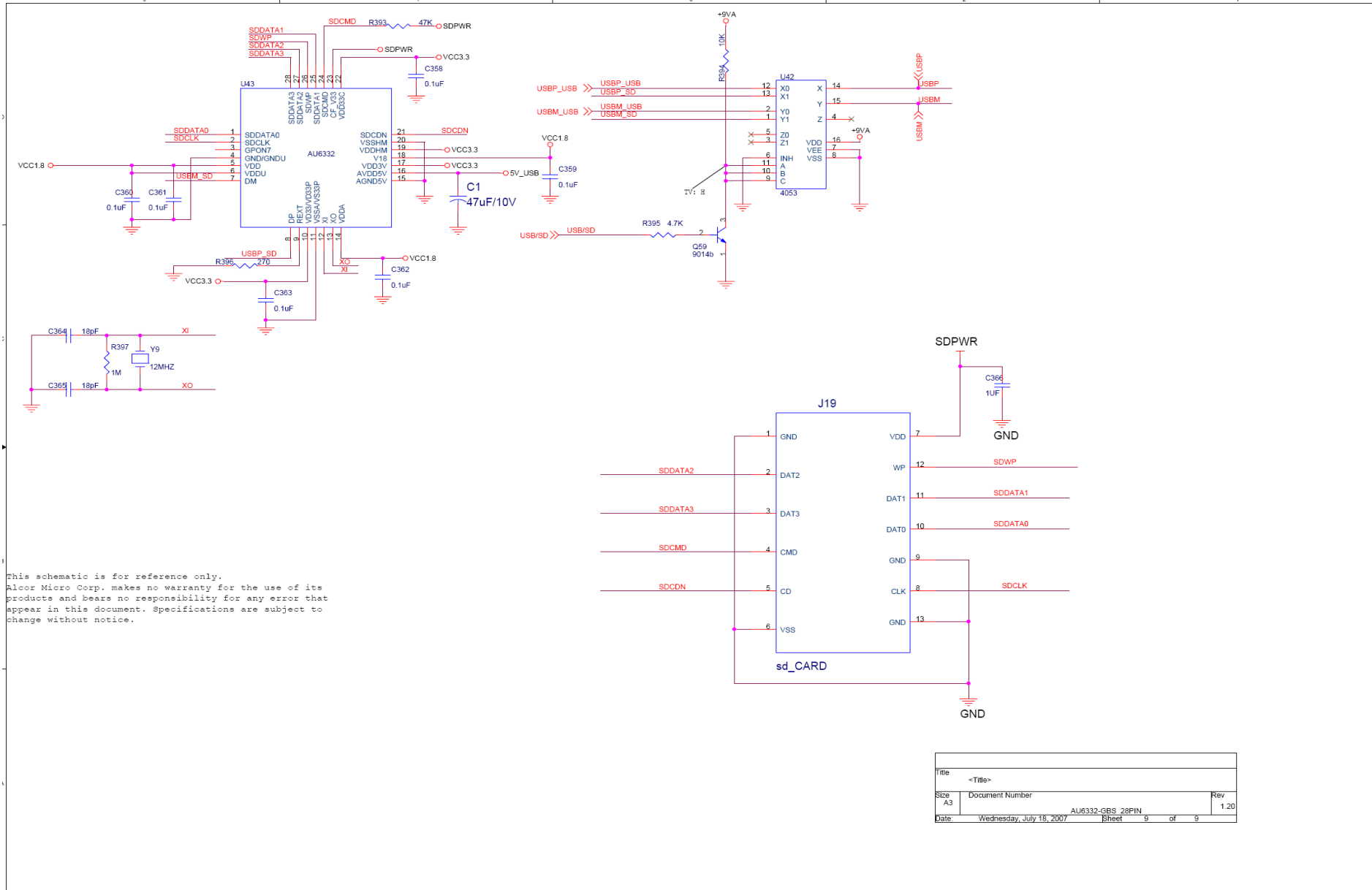


SHEN ZHEN HCN ELECTRONICS CO.,LTD				
REVISION	MOD RECORD	DOCUMENT: PDT2920		
REV.0	2006/08/12	TITLE: MCU		
		DRAWER:	DESIGN: rxliang	APPROVED: [Signature]
				ITEM NO: [Blank]
				PAGE 6 OF 9

DVD Part



DVD Part



This schematic is for reference only. Alcor Micro Corp. makes no warranty for the use of its products and bears no responsibility for any error that appear in this document. Specifications are subject to change without notice.

Title		
<Title>		
Size	Document Number	Rev
A3	AU6332-GBS 28PIN	1.20
Date	Wednesday, July 18, 2007	Sheet 9 of 9